CERAMIC CAPACITOR Catalog

2022

2023

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You Should Know Holy Stone.

The Company

Holy Stone Enterprise Company Co., Ltd. (Holy Stone) is headquartered in the Nei Hu District of Taipei, Taiwan. The company was founded in June of 1981 by three engineers as a technology focused distributor of electronic components.

In 1999, with technology and cooperation from a Japanese partner, Holy Stone began manufacturing Multi-layer Ceramic Capacitors. Today, Holy Stone is recognized as an industry leader in application specific ceramic capacitors.

Holy Stone integrates active and passive component distribution with state-of-the-art Ceramic Capacitor manufacturing capabilities. Holy Stone's unique business model combines the service and inventory management strengths of a broad line distributor with the technical knowledge and world class pricing of a manufacturer.

Holy Stone maintains a focus on and a commitment to providing customers with innovative products and exceptional service. That unwavering commitment is evident in Holy Stone's phenomenal growth.

Holy Stone maintains a high profile on the Taiwan Stock Exchange, is ranked in the top 100 companies and is recognized as a leader among its peers.

Manufacturing Facilities

Holy Stone ceramic capacitors are produced in two modern factories located in Lungtan and Yilan, Taiwan. Holy Stone advanced materials research laboratory is located in Japan. The factory operating systems are certified to ISO-9001, IATF16949 and ISO-14001.

Sales and Support Locations

Holy Stone Headquarter and Corporate Sales are located in Taipei, Taiwan. Holy Stone also maintains sales and support offices in Shenzhen, Suzhou and Shanghai, China. Holy Stone Enterprise Company Co., Ltd. has subsidiary companies in North America and in Europe. "HolyStone International", located in Diamond Bar, California, U.S.A. covers sales and technical support in the America. "Holystone (Europe) Ltd.", located in Norwich, England is responsible for sales and technical support throughout Europe. "Holy Stone Holdings Pte., Ltd." located in Singapore provides sales and support in South and Southeast Asia. Holy Stone also has a global network of independent representatives, agents and distributors.

The Environment

Holy Stone is committed to achieving and maintaining a healthy environment. The factories are certified to ISO-14001 and all standard products are designed and produced in compliance to RoHS.

Our Employees

Holy Stone's success is measured by the satisfaction of our customers and share holders. Achieving that satisfaction is the result of the sum contribution of our employees. Holy Stone strives to maintain a work environment that stimulates creativity, encourages enthusiasm, recognizes and rewards results.



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Capacitance Availability Guide

Vdc	Dielectric	0201	0402	0603	0805	1206	1210	1808	1812	1825	2220	2225
	NP0				22nF	100nF						
6.3V	X7R	100nF	1uF	4.7uF	10uF	22uF						
	X5R	4.7uF	22uF	47uF	100uF	100uF	220uF					
	NP0				22nF	100nF					4	
10V	X7R		100nF	4.7uF	10uF	22uF	47uF					
	X5R	1uF	22uF	22uF	47uF	47uF	100uF					
	NP0	100pF		3.9nF	22nF	100nF	100nF		220nF	100nF		
16V	X7R	3.9nF	470nF	1uF	10uF	10uF	22uF					
	X5R		4.7uF	10uF	22uF	47uF	100uF					
	NP0	100pF		3.9nF	22nF	100nF	100nF		220nF	100nF		
25V	X7R	2.2nF	220nF	1uF	4.7uF	10uF	22uF		10uF		10uF	4.7uF
	X5R		2.2uF	10uF	22uF	22uF	22uF					
	NP0				22nF	100nF						
35V	X7R				2.2uF	10uF	10uF				1	
	X5R	-	2.2uF	10uF	10uF							
	NP0		1nF	3.9nF	22nF	100nF	100nF		220nF	100nF	27nF	82nF
50V	X7R	10nF	100nF	1uF	2.2uF	4.7uF	10uF		10uF	2.2uF	10uF	4.7uF
	X5R		10000	2.2uF	10uF	10uF	10uF				3.5.5.	
	NP0			3.9nF	12nF	100nF	100nF		100nF	100nF	27nF	82nF
100V	X7R	1nF	10nF	220nF	1uF	3.3uF	4.7uF		2.2uF	1uF	10uF	4.7uF
1000	X5R		.,			2.2uF						
	NP0			560pF	4.7nF	22nF	47nF		100nF	100nF	33nF	33nF
200V	X7R			10nF	100nF	220nF	680nF	560nF	1uF	1uF	2.2uF	2.2uF
00.00	X5R								2			
	NP0			560pF	4.7nF	22nF	47nF		100nF	100nF	33nF	33nF
250V	X7R		100pF	47nF	100nF	220nF	560nF	560nF	1uF	1uF	2.2uF	2.2uF
	X5R											
	NP0				1.5nF	10nF	33nF	2.2nF	33nF	100nF	27nF	15nF
500V	X7R				22nF	68nF	120nF	47nF	470nF	470nF	1uF	470nF
77.7	X5R											
	NP0				3.9nF	10nF	33nF	2.2nF	33nF	47nF	22nF	15nF
630V	X7R				10nF	47nF	100nF	47nF	220nF	330nF	470nF	100nF
	X5R											
	NP0				1.5nF	3.3nF	15nF	2.2nF	2.2nF	12nF	33nF	15nF
1KV	X7R				2.2nF	22nF	47nF	33nF	100nF	150nF	220nF	100nF
	X5R											
	NP0					390pF	100pF	1nF	1.5nF		6.8nF	10nF
2KV	X7R					4.7nF	4.7nF	10nF	33nF	22nF	39nF	47nF
	X5R											
	NP0					47pF		1nF	1.2nF		2.2nF	3.3nF
зку	X7R					1nF	2.7nF	5.6nF	10nF	18nF	10nF	22nF
	X5R							3.3111				
	NP0							220pF				
4KV	X7R							1nF			10nF	4.7nF
1	X5R										10111	777111
	NP0										150pF	56pF
5KV	X7R							1nF			4.7nF	ООР
200	X5R							11.11			3.7111	

Capacitance Availability Guide – Safety Certified Capacitors



Safety Certified SMD Capacitors										
Class	Vac	Dielectric	1808	1812	2208	2211	2220	2825		
V4.0/0	250Vrms	NPO	2.0pF - 330pF	2,0pF - 680pF	2.0pF - 330pF	2.0pF - 1nF	2.0pF - 1.2nF	N/A		
X1/Y2	250Vrms	X7R	150pF - 1nF	130pF - 1nF	36pF - 1nF	68pF - 2.7nF	100pF - 4.7nF	N/A		
	250Vrms	NPO	2.0pF -1nF	N/A	N/A	N/A	N/A	N/A		
X2	250Vrms	X7R	150pF - 2.2nF	330pF - 4.7nF	N/A	N/A	150pF - 68nF	47nF - 56nF		
	305Vrms	X7R	N/A	N/A	N/A	N/A	150pF - 33nF	N/A		



All ranges are Lead (Pb) free



ISO Certification

Plant	Certificated	Date	Organization	Registration No.
Taipei HQ/Lung Tan Factory	ISO 9001:2015	20 March, 2002	BVC	TWN4523166Q
Taipei HQ/Lung Tan Factory	ISO 14001:2015	29 May, 2003	BVC	TW005163
Taipei HQ/Lung Tan Factory	IATF16949:2016	27 September, 2016	BVC	41453
Taipei HQ/Lung Tan Factory	IECQ QC080000:2017	14 May, 2020	BVC	IECQ-H LCIE 20.0011

ISO 9001:2015



ISO 14001:2015



IECQ QC 080000:2017



IATF16949: 2016





[Middle Voltage Capacitors - NPO,X7R,100Vdc to 630Vdc]

THE REAL PROPERTY.

MVC Series

Holy Stone high voltage products are designed and manufactured to meet the general requirements of international standards. The product offering is well suited for commercial and industrial applications and includes NP0 (C0G) and X7R characteristics in sizes 0402 to 2225 and with working voltages up to 630Vdc.

Features

- □ Special internal electrode design offers the highest voltage rating
- Surface mount suitable for wave and reflow soldering
- ☐ High reliability
- □ RoHS compliant

Applications

- □ Suitable for LAN/WLAN interface, Back-Lighting Inverter, DC-DC Converters, Ballast, Modems and Power Supplies.
- ☐ SiC & GaN systems, Snubber, Resonant Circuit (LLC, Wireless Charging, etc.)

Summary of Specifications

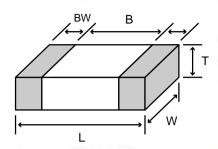
Operation Temperature	-55 °C to +125 °C						
Rated Voltage	100Vdc to 630Vdc						
T	NP0 : ≤ ± 30ppm/ °C , -55 °C to +125 °C (EIA Class I)						
Temperature Coefficient	X7R : ≤ ± 15% , -55 °C to +125 °C (EIA Class Ⅱ)						
Dissipation Factor	NP0 : More than 30pF : Q ≧1000 30pF & Below : Q≧400+20C (C : Capacitance , pF) X7R : 100V : 5% (C≧0.1uF) 100V : 2.5% (C<0.1uF) Other Voltage : 2.5% max						
Insulation Resistance	10GΩ or 500/CΩ, whichever is smaller						
Aging	NP0:0% , X7R: Typically 1.0% per decade of time						
	100V ≦ V < 500V : 200% Rated Voltage						
Dielectric Strength	500V ≦ V < 1000V : 150% Rated Voltage						
	1000V≦ V : 120% Rated Voltage						

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Thickness (mm) (Optional)	Special Requirement (Optional)	Suffix Code
C: MLCC (Multilayer Ceramic Capacitor)	Ex.: 0402 0603 0805 1206 1210 1808 1812 1825 2220 2225	Ex.: N: NP0 X: X7R	Ex.: 2R0:2.0pF 100:10×10° 471:47×10¹ 102:10×10²	Ex.: C:+/-0.25pF D:+/-0.50pF J :+/- 5% K :+/-10% M:+/-20%	Ex.: 101: 100Vdc 251: 250Vdc 501: 500Vdc 631: 630Vdc	Ex. : T: T&R 7" R: T&R 13" B: Bulk	Ex: E:1.60±0.20 F:2.0±0.20	Ex.: O: Arc Prevention Coating X: Polymer Termination (Super Term) Z: Coating & Polymer Termination	Y

MVC Series - Middle Voltage Capacitors (100Vdc to 630Vdc)



Dimensions



SIZE	La La La	W	T (max)	B (min)	BW (min)
0402	1.00±0.05	0.5±0.05	0.55	0.30	0.15
	[.039±0.02]	[.020 ±0.02]	[.022]	[.012]	[.006]
0603	1.60±0.10	0.80±0.10	1.00	0.40	0.15
	[.063±.004]	[.031 ±.004]	[.039]	[.016]	[.006]
0805	2.00±0.20	1.25±0.20	1.45	0.70	0.20
	[.079±.012]	[.049±.012]	[.057]	[.028]	[.008]
1206	3.20±0.30	1.60±0.20	1.80	1.50	0.30
	[.126±.012]	[.063±.012]	[.071]	[.059]	[.012]
1210	3.20±0.30	2.50±0.20	2.60	1.60	0.30
	[.126±.012]	[.098±.012]	[.102]	[.059]	[.012]
1808	4.60±0.30	2.00±0.20	2.20	2.50	0.30
	[.181±.012]	[.079±.008]	[.087]	[.098]	[.012]
1812	4.60±0.30	3.20±0.30	3.00	2.50	0.30
	[.181±.012]	[.126±.012]	[.118]	[.098]	[.012]
1825	4.60±0.30	6.35±0.40	3.40	2.50	0.30
	[.181±.012]	[.250±.016]	[.118]	[.098]	[.012]
2220	5.70±0.40	5.00±0.40	3.00	3.50	0.30
	[.220±.016]	[.197±.016]	[.118]	[.137]	[.012]
2225	5.70±0.40	6.35±0.40	3.00	3.50	0.30
	[.220±.016]	[.250±.016]	[.118]	[.137]	[.012]

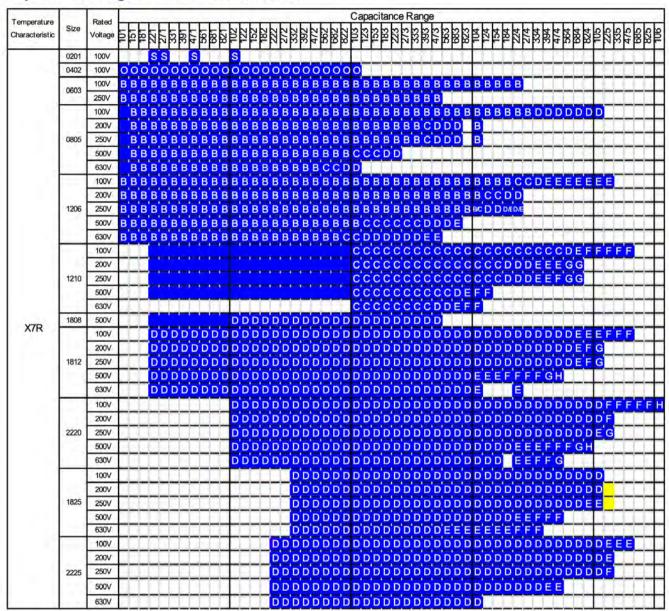
◆ Capacitance Range - NP0 / 100Vdc to 630Vdc

Temperature	GE	Rated	Capacitance Range
Characteristic	Size	Voltage	883
	0402	250V	0000000000000000000
		100V	888888888888888888888888888888888888
	0603	200V	888888888888888888888888888888888888
		250V	8888888888888888888888888888
		100V	B B B B B A A A A A A A A A B B B B B B
	0805	200V	B B B B B A A A A A A A A A A B B B B B
	0805	250V	B B B B B A A A A A A A A A A B B B B B
		500V	B'B'B'B'B'A'A'A'A'A'A'A'A'BB'B'B'B'B'B'
		100V	C'C'C'C'C'C'C'B'B'B'B'B'B'B'B'B'B'B'B'B
	4 14 1	200V	CCCCCCCBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB
	1206	250V	C'C'C'C'C'C'C'B'B'B'B'B'B'B'B'B'B'B'B'B
		500V	
121		630V	C'C'C'B'B'B'B'B'B'B'B'B'B'B'B'B'B'B'B'B
	-	100V	CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
		200V	CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
	1210	250V	CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCC
		500V	CCCCCCCCCCCCCCCCCCCCCCCCCCCCDDDEEFFGGGGGG
NP0		630V	CCCCCCCCCCCCCCCCCCCCCCCCCCCCCCCDDDEEFFGGGGGG
300.3	4000	500V	DDDDDDDDDDDDDDDDDDDDDDDDDDDEE
	1808	630V	DDDDDDDDDDDDDDDDDDDDDDDDDDDDEE
		100V	
		200V	DDDDDDDDDDDDDDDDDDDDDDDDDDDDDDDEEEE
	1812	250V	
	100	500V	DDDDDDDDDDDDDDDDDDDDDDDDDDDD
		630V	DODDDDDDDDDDDDDDDDDDDDDDDDDDDDDD
93		250V	DDDDDEEEEFFFF
	1825	500V	DEEEEFFFGG
		630V	DEFEFFGGG
93		100V	D D D D D D D D
	213	250V	DEED CONTRACTOR OF THE PROPERTY OF THE PROPERT
	2220	500V	D DD D
		630V	
- 0		100V	D D D D D D D D D D D D D D D D D D D
	2225	250V	D D D D D D D D D D D D D D D D D D D
		500V	FEE TO SELECT

MVC Series - Middle Voltage Capacitors (100Vdc to 630Vdc)



◆ Capacitance Range – X7R / 100Vdc to 630Vdc



- The yellow indication denotes values that are under development. Please contact Holy Stone office for further details
- Other dimensions, capacitance values and voltages ratings are available on request. Please contact Holy Stone.

Thickness Specification

Symbol Code	S	0	Α	В	С	D	E	F	G	Н	1
Thickness(mm)	0.3±0.03	0.5±0.05	0.6±0.1	0.85±0.15	1.0+0.1/-0.05	1.25±0.20	1.6±0.2	2.0±0.2	2.4±0.2	2.8±0.2	3.2±0.2



[High Voltage Capacitors - NPO, X7R 1KVdc to 5KVdc]

HVC Series



Holy Stone high voltage products are designed and manufactured to meet the general requirements of international standards. The product offering is well suited for commercial and industrial applications and includes NP0 (C0G) and X7R characteristics in sizes 0805 to 2225 and with working voltages from 1KV up to 5KV.

Features

- Special internal electrode design offers the highest voltage rating
- Surface mount suitable for wave and reflow soldering
- ☐ High reliability
- ☐ RoHS compliant

Applications

- □ Suitable for LAN/WLAN interface, Back-Lighting Inverter, DC-DC Converters, Ballast, Modems and Power Supplies.
- ☐SiC & GaN systems, Snubber, Resonant Circuit (LLC, Wireless Charging, etc.)

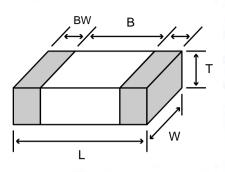
Summary of Specifications

Operation Temperature	-55 °C to +125 °C						
Rated Voltage	1KVdc to 5KVdc						
Tamanatura Caafficiant	NP0 : ≤ ± 30ppm/ °C , -55 °C to +125 °C (EIA Class I)						
Temperature Coefficient	X7R : ≤ ± 15% , -55 °C to +125 °C (EIA Class Ⅱ)						
Dissipation Factor	NP0 : More than 30pF : Q ≧1000 30pF & below : Q≧400+20C (C : Capacitance , pF) X7R : D.F.≦ 2.5%						
Insulation Resistance	10GΩ or 500/CΩ, whichever is smaller						
Aging	NP0: 0% , X7R : Typically 1.0% per decade of time						
	100V ≦ V < 500V : 200% Rated Voltage						
Dielectric Strength	500V ≦ V < 1000V : 150% Rated Voltage						
	1000V≦ V : 120% Rated Voltage						

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Thickness (mm) (Optional)	Special Requirement (Optional)	Suffix Code
C: MLCC (Multilayer Ceramic Capacitor)	Ex.: 0805 1206 1210 1808 1812 1825 2220 2225	Ex.: N: NP0 X: X7R	Ex.: 2R0:2.0pF 100:10×10 ⁰ 471:47×10 ¹ 102:10×10 ²	Ex.: C: +/-0.25pF D: +/-0.50pF J: +/- 5% K: +/-10% M: +/-20%	Ex.: 102: 1000Vdc 202: 2000Vdc 302; 3000Vdc 402: 4000Vdc 502: 5000Vdc	Ex.: T: T&R 7" R: T&R 13" B: Bulk	Ex: E:1.60±0.20 F:2.0±0.20 I:3.2±0.20	Ex.: O: Arc Prevention Coating X: Polymer Termination (Super Term) Z: Coating & Polymer Termination	Y



Dimensions



				Uni	t : mm [inche
SIZE	L	W	T (max)	B (min)	BW (min)
0805	2.00±0.20	1.25±0.20	1.45	0.70	0.20
	[.079±.012]	[.049±.012]	[.057]	[.028]	[.008]
1206	3.20±0.30	1.60±0.20	1.80	1.50	0.30
	[.126±.012]	[.063±.012]	[.071]	[.059]	[.012]
1210	3.20±0.30	2.50±0.20	2.60	1.60	0.30
	[.126±.012]	[.098±.012]	[.102]	[.059]	[.012]
1808	4.60±0.30	2.00±0.20	2.20	2.50	0.30
	[.181±.012]	[.079±.008]	[.087]	[.098]	[.012]
1812	4.60±0.30	3.20±0.30	3.00	2.50	0.30
	[.181±.012]	[.126±.012]	[.118]	[.098]	[.012]
1825	4.60±0.30	6.35±0.40	3.00	2.50	0.30
	[.181±.012]	[.250±.016]	[.118]	[.098]	[.012]
2220	5.70±0.40	5.00±0.40	3.40	3.50	0.30
	[.220±.016]	[.197±.016]	[.118]	[.137]	[.012]
2225	5.70±0.40	6.35±0.40	3.40	3.50	0.30
	[.220±.016]	[.250±.016]	[.118]	[.137]	[.012]

◆ Capacitance Range - NP0 / 1KVdc to 2KVdc

Temperature	(Sept.	Rated	Capacitance Range
Characteristic	Size	Voltage	2222333332222333222233332223333322333333
	0805	1KV	BBBBBBBBCCCCCCCCCCCCCDD
	1206	1KV	
	1200	2KV	D'D'D'D'D D'D'D'D'D'D'D'D'D'D'D'D'D'D'D
	1210	1KV	D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'
	1210	2KV	
	1808	1KV	D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'
NP0	1000	2KV	D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'
	1812	1KV	(DD'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D
	1012	2KV	
	2220	1KV	D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'D'E'F'G'GGI'I
	2220	2KV	
	2225	1KV	
	2225	2KV	D'D'D'D'D'D'D'D'D'D'E'E'E'G'G'G'H I

◆ Capacitance Range – NP0 / 3KVdc to 5KVdc

Temperature	-	Rated									C	ара	acit	and	ce l	Rar	ige									
Characteristic	Size	Voltage	2R0	3R9	8R0	842	128	150	220	270	390	470	260	820	10	151	181	221	331	391	471	681	821	102	152	182
	1206	3KV	D D	DE	D	DI	D D	D	D D	E	E	Ø		11:	Г		H			1		1	1		E	
	1808	3KV	D D	D D	ď	DI	ם'כ	D'	D'D	D'	D'C	ď	Di	ם'ם	D	ΕΈ	F	F	F F	F	FΊ	Ē	F	F		
	1000	5KV	FF	FF	F	F	F F	F	F'F	F	F'F	F														
	1812	3KV	D ['] D	D'D	ďD	DI	ם כ	D'I	D'D	D'	D'C	ם י	D'I	D'D	E	E E	Ε	E.	EΈ	Ε	ΕĖ	E'F	F	G		
NP0	1012	4KV					ם'כ	D	D'D	ם'	D'C	ם ו	D'I	ם'כ	E,	E'E	ΪF	G								
INFO	2208	3KV	FF	FF	F	F	F F	F.	F F	F	F F	Г		T.			1		H				1			
	2211	3KV	FF	FF	F	FI	F F	F.	F F	F	F F	F	F	1												
	2220	3KV	D D	D'D	ď	DΙ	ם'כ	Di	D D	ם'	E E	Έ	ΕÏ	EΈ	E	E E	Ε	ΕÏ	EΈ	Έ	E E	ĒΕ	Έ	F	F F	F
	2220	5KV	D'D	D'D	D'	DI	ס'כ	D	ם ס	^י ם	E E	Έ	ΕÍ	EΈ	E.	E E			- -				E	1		1-1
	2225	5KV	1										D			-										

- The yellow indication denotes values that are under development. Please contact Holy Stone office for further details
- Other dimensions, capacitance values and voltages ratings are available on request. Please contact Holy Stone.

Thickness Specification

Symbol Code	В	С	D	E	F	G	H	
Thickness(mm)	0.85±0.15	1.0+0.1/-0.05	1.25±0.20	1.6±0.2	2.0±0.2	2.4±0.2	2.8±0.2	3.2±0.2

HVC Series – High Voltage Capacitors



♦ Capacitance Range – X7R / 1KVdc

Temperature		Rated	Capacitance Range
Characteristic	Size	Voltage	888 4 38 37 27 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 37 38 38 37 38 38 37 38 38 37 38 38 38 38 38 38 38 38 38 38 38 38 38
	0805	1KV	BBBBBBBBBBBBBBB
	1206	1KV	B B B B B B B B B B B B B B B B B B B
	1210	1KV	CCCDDDDDDEFFG
X7R	1808	1KV	DDDDDDDDDDDDDDD
AIK	1812	1KV	DDDDDDDDDDDDDDDDDDDDDDDDDEFFGG
	2220	1KV	DDDDDDDDDDDDDDDDDDDDDDEEFFF
	1825	1KV	DDDDDDDDDDDDEEEEGG
	2225	1KV	D D D D D D D D D D D D D D D D D D D

◆ Capacitance Range - X7R / 1.5KV to 2.5KV

Temperature		Rated		١.				1				ř.,				. (Cap	oac	ita	nc	e R	an	ge	ŞI,						. n			-	7		Π
Characteristic	Size	Voltage	151	181	221	271	331	391	471	561	189	821	102	122	152	182	222	717	332	392	472	299	682	822	103	123	153	183	223	273	303	473	563	683	823	104
	200	1.5KV	В	В	В	В	В	В	В	В	В	В	В	С	C	C	C	C	С	С	D									-1			E			
	1206	2KV	В	В	в	D	D	D	D	D	D,	D	в	С	D.	D	D	D.	D.	E.	ΕÌ						П			T			Т	Г		
		2.5KV	В	В	в	В	в	В	в	В	в	В	В	С	c	c	D,	D,	E	E	Е			-							T	I		Е		
1.5	1210	2KV		1	c	c	c	c	С	С	c	С	c	C	c'	c	D	D,	E	E	E,	E	F	F	G						1					
X7R	1808	2KV	D	Ö	ď	ď	D	ď	D	D	D,	D	D	D	D.	D	D	D,	D	D	D,	D,	D,	Ε	Ε						4					
777	1812	2KV			D	D	D	ď	D	D	ď	D	D	D,	D,	D	D	D,	D	D.	D,	ם י	D	D	Ε	Ε	Ε	F	F	G	1					
	1825	2KV				П							Т	_	Ξ,	_	_		D	D	D,	D,	D	D	D	Ε	E	Ε	E	F)	F (3 (3	Г		
	2220	2KV											D,	D	D,	D	D	D,	D	D	D	D,	D	D	D	D	ם	E	Ε	E'	F F	= (
	2225	2KV											D	D	D.	D	D	D,	D	D	D,	D,	D	D	D	D	D	D	D	E'	E E	Ē		I	П	

♦ Capacitance Range – X7R / 3KVdc to 5KVdc

E		104														C	apa	aci	tan	ce	Ra	ng	е											
Temperature Characteristic	Size	Rated Voltage	151	181	221	271	331	391	471	561	681	821	102	122	152	182	222	272	332	392	4/4	200	700	770	202	2 5	3 8	223	273	333	393	473	563	683
	1206	3KV	B	В	В	В	В	С	С	С	D	D	D							П		1	ij	1		1	- 1	7		1	E			ij.
	4000	3KV	C	o c	Ď	ď	ď	D	D	D	D	D	D	b	D	D	D	E	E.	E.	F	FΪ		7			П				ī			
	1808	4KV	C	ď	Ď	Ġ	D	E	E	E	F	F	F									T	T	7										
	1812	3KV	Т	۲	۵,	Ď	D	D	ď	D	D	D	D	D	D	D	D	D	D.	ם	E.	E.	E.	F	F					1				
van	1825	3KV		1	1				_		D	D	D	ם	D	D	D,	D	D,	D'	ם'	D.	D.	Е	E'	E I	F (3	Т		Г			
X7R	2220	3KV	T	T	1	٦		_	_		D	D	D	D	D.	D	D,	D	D.	D,	D.	D.	D.	Е	εÌ		T	T	T					
	2220	4KV		۰	•	۳	_	_	_	_	_		D	D	D	D	E,	E	E,	E'	F.	F.	F.	G	н		Т	T	T	Т	Г	П		
	2220	5KV	0	ם ס	Ď	Ď	D	ם	ď	D	D	D	D	ם	D	E	E,	E,	F,	F.	ϝĬ			7		7	T	T	Т	Т	П			
	2225	3KV	T	۲	1		_	_	_		_				_		ם'	D	ם'	ם'	oʻ	D.	D ^T	D	D [']	D.	ì	Ť	۲		Т			1
	2225	4KV	T	٠	1		۳	_	_	_	_			_	_	_	D,	D	Ē,	Ė,	εŤ			1				T						- 1

Thickness Specification

Symbol Code	В	С	D	E	F	G	H	
Thickness(mm)	0.85±0.15	1.0+0.1/-0.05	1.25±0.20	1.6±0.2	2.0±0.2	2.4±0.2	2.8±0.2	3.2±0.2



[Normal Chip Capacitors - less than 1uF]

NCC Series



Unit: mm [inches]

Standard Multilayer Ceramic Capacitors are available in a full range of sizes and temperature coefficients, with voltage ratings from 6.3V to 50V.

Features

- Surface mount suitable for wave and reflow soldering
- ☐ Small size and high reliability
- ☐ Excellent in high frequency characteristics
- ☐ RoHS compliant

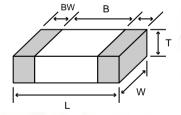
Applications

□ Suitable for general electronics circuit, telecomm2unication, personal computers and peripheral, power circuit, mobile application, etc.

Summary of Specifications

Operation Temperature	NP0/X7R/X7S : -55 °C to +125 °C , X6R : -55 °C to +105 °C, X5R : -55 °C to +85 °C
Rated Voltage	6.3Vdc to 50Vdc
	NP0 : ≤ ± 30ppm/ °C , -55 °C to +125 °C (EIA Class I)
	X7R : ≤ ± 15% , -55 °C to +125 °C (EIA Class II)
Temperature Coefficient	X7S : ≤ \pm 22% , -55 °C to +125 °C (EIA Class Π)
	X6S : ≤ \pm 22% , -55 °C to +105 °C (EIA Class Π)
	X5R : ≤ ± 15% , -55 °C to +85 °C (EIA Class II)
Dissipation Factor	NP0 : More than 30pF : Q ≥1000 30pF & below : Q≥400+20C (C : Capacitance , pF)
	X7R/X7S/X6S/X5R : 10% max
Insulation Resistance	10,000mΩ or 500/CΩ, whichever is smaller for rated voltage>10V and greater than 100/CΩ for rated voltage≤10V.
Aging	NP0: 0%, X7R/X7S/X6S/X5R: typically 1.0%
Dielectric Strength	250% Rated Voltage

Dimension



SIZE	P	W	T (max)	B (min)	BW (min)
UILL			I (IIIax)	D (min)	DVV (IIIIII)
0201	0.60±0.09	0.30±0.09	0.39	0.20	0.10
	[.024±.003]	[.011 ±.003]	[.015]	[.008]	[.004]
0402	1.00±0.05	0.50±0.05	0.55	0.30	0.15
	[.039±.002]	[.020 ±.002]	[.022]	[.012]	[.006]
0603	1.60±0.10	0.80±0.10	1.00	0.40	0.15
	[.063±.004]	[.031 ±.004]	[.039]	[.016]	[.006]
0805	2.00±0.20	1.25±0.20	1.45	0.70	0.20
	[.079±.012]	[.049 ±.008]	[.057]	[.028]	[.008]
1206	3.20±0.30 [.126±.012]	1.60±0.20 [.063±.008]	1.80	1.50 [.059]	0.30

С	0201	R	104	K	010	T	S	Υ
Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Thickness (mm) (Optional)	Suffix Code
C: MLCC (Multilayer Ceramic Capacitor)	Ex.: 0201 0402 0603 0805 1206	Ex.: N: NP0 X: X7R R: X7S S: X6S B: X5R	Ex.: 102 : 10x10 ² 103 : 10x10 ³	Ex.: F:+/- 1% G:+/- 2% J:+/- 5% K:+/- 10% M:+/- 20%	Ex.: 004 : 4Vdc 007 :6.3Vdc 010 :10Vdc 016 :16Vdc 025 :25Vdc 035 :35Vdc 050 :50Vdc	Ex.: T: T&R 7" R: T&R 13" B: Bulk	Ex: E:1.6±0.20 F:2.0±0.20 S:0.3±0.03	Y



◆ Capacitance Range

Dielectric		Rated	Capacitance Range
Characteristic	Size	Voltage	200
	0201	16V	SSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSS
	0201	25V	S S S S S S S S S S S S S S S S S S S
NPO	0402	50V	000000000000000000000000000000000000000
	0603	50V	BBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB
-1 /	0805	50V	B B B B B B B B B B B B B B B B B B B

Dielectric	200	Dated	Capacitance Range
Characteristic	Size	Rated Voltage	101 101 101 101 101 101 101 101 101 101
		6.3V	S S
	0201	16V	SSSSS
	0201	25V	SSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSS
		50V	SSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSSS
l I		10V	O T
	0400	16V	0000000000000
	0402	25V	000000000000000000000000000000000000000
		50V	000000000000000000000000000000000000000
		10V	BBBBB
X7R	0000	16V	
	0603	25V	BBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB
		50V	
		16V	BBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB
	0005	25V	BBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB
	0805	35V	BBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB
		50V	BBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBBB
		16V	BBBBBB
	1206	25V	B B B C D D
		50V	BBBCDDE

Dielectric	Size	Rated	Capacitano	ce Range
Characteristic	Size	Voltage	273 333 347 473 563 823	104 224 334 474 684
	7977	6.3V		S S2
X7S	0201	10V		S
		16V		S

Dielectric Characteristic	Size	Rated Voltage	Capacitance Range
		6.3V	S S2S2
¥00	2004	10V	S S2S2
X6S	0201	16V	S
1 2 7 2		25V	S

Dielectric	Cina	Rated	Capacitance Range
Characteristic	Size	Voltage	273 333 393 393 104 104 174 474 684
		6.3V	S S S2 S2
	0201	10V	S S2 S2 S2
	0201	16V	S S2 S2
		25V	S S2 S2
1		6.3V	0 0
X5R	0400	10V	0 0 0
ASK	0402	16V	00000000
		25V	00000000
		6.3V	ВВ
	0000	10V	B B B B B B
	0603	16V	B B B B B B
-		25V	B B B B B

- The yellow indication denotes values that are under development. Please contact Holy Stone office for further details
- Other dimensions, capacitance values and voltages ratings are available on request. Please contact Holy Stone.

Thickness Specification

■ Specifications & Test Conditions please see P51~P55

Symbol Code	S	S2	0	T	Α	В	С	D	E
Thickness(mm)	0.3±0.03	0.3±0.09	0.5±0.05	0.5±0.1	0.6±0.1	0.85±0.15	1.0+0.1/-0.05	1.25±0.20	1.6±0.2



[High Capacitance MLCCs - 1.0uF and above]

HCC Series

Features

- Surface mount suitable for wave and reflow soldering
- ☐ High reliability
- ☐ Small size and high capacitance value
- ☐ Excellent high frequency characteristics
- □ RoHS compliant

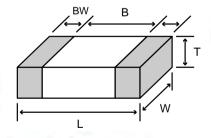
Applications

- ☐ Ideal for smoothing and decoupling circuits
- Suitable for DC-DC converter, personal computer and peripherals, telecommunication and general electronic equipment

Summary of Specifications

Operation Temperature	X7R / X7S : -55 °C to +125 °C , X6S : -55 °C to +105 °C; X5R : -55 °C to +85 °C
Rated Voltage	4.0Vdc to 50Vdc
	X7R : ≤ ± 15% , -55 °C to +125 °C (EIA Class Ⅱ)
T	X7S : ≤ \pm 22% , -55 °C to +125 °C (EIA Class Π)
Temperature Coefficient	X6S : ≤ \pm 22% , -55 °C to +105 °C (EIA Class Π)
	X5R : ≤ ± 15% , -55 °C to +85 °C (EIA Class II)
Dissipation Factor	X7R, X5R, X6S, X7S : 15% max
Insulation Resistance	10,000mΩ or 500/CΩ, whichever is smaller for rated voltage>10V and greater than 100/CΩ for rated voltage≤10V
Aging	X7S/X7R/X6S/X5R : typically 1.0%
Dielectric Strength	250% Rated Voltage

Dimension



				Ur	nit : mm [inche
SIZE	L	W	T (max)	B (min)	BW (min)
0201	0.60±0.09	0.30±0.09	0.55	0.20	0.10
	[.024±.003]	[.011 ±.003]	[.022]	[.008]	[.004]
0402	1.00±0.05	0.50±0.05	0.95	0.30	0.15
	[.039±.002]	[.020 ±.002]	[.037]	[.012]	[.006]
0603	1.60±0.10	0.80±0.10	1.00	0.40	0.15
	[.063±.004]	[.031 ±.004]	[.039]	[.016]	[.006]
0805	2.00±0.20	1.25±0.20	1.45	0.70	0.20
	[.079±.012]	[.049 ±.008]	[.057]	[.028]	[.008]
1206	3.20±0.30	1.60±0.20	1.90	1.50	0.30
	[.126±.012]	[.126±.012]	[.074]	[.059]	[.012]
1210	3.20±0.30	2.50±0.20	2.80	1.60	0.30
	[.126±.012]	[.098±.008]	[.110]	[.063]	[.012]

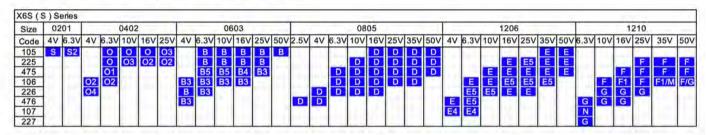
Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Thickness (mm) (Optional)	Suffix Code
C: MLCC (Multilayer Ceramic Capacitor)	Ex.; 0201 0402 0603 0805 1206 1210	Ex.: N:NP0 X:X7R R:X7S S:X6S B:X5R	Ex.: 105:10x10 ⁵ 225:22x10 ⁵ 475:47x10 ⁵ 106:10x10 ⁶ 226:22x10 ⁶	Ex.: F:+/-1% G:+/-2% J:+/-5%	Ex.: 004 : 4Vdc 007 :6.3Vdc 010 :10Vdc 016 :16Vdc 025 :25Vdc 035 :35Vdc 050 :50Vdc	Ex.: T: T&R 7" R: T&R 13" B: Bulk	Ex: E:1.6±0.20 F:2.0±0.20 O:0.5±0.05	Y



♦ Capacitance Range

Size	04	102			06	603			111		30	305					12	06			1		1210)	
Code	4V	6.3V	6.3V	10V	16V	25V	35V	50V	6.3V	10V	16V	25V	35V	50V	6.3V	10V	16V	25V	35V	50V	10V	16V	25V	35V	50V
105	0	0	В	B5	B5	B5	B5	B4	D	D	D	D	D	D	D	D	D/E	E5	D/E	E5	D	D	D	D	E
225			B5	B5					D	D	D	D	D	D	Е	E5	E5	E5	E5	E5	F	F	F	F	F
475			В3	В3		<u> </u>			D	D	D	D			Е	E5	E5	E5	E5	E5	F	F	F	F	F
106									D	D	D				E5	E5	E5	E5	E5		F	F1	F1	F1/M	F1/M
226															E5	E5				1	M	M	M		
476	-																				G				

Size	04	02		0603			0805		1206	1210
Code	6.3V	10V	10V	16V	25V	16V	25V	50V	16V	6.3V
105		01				-				
225	02	02		В	В					
475			B4	В3				D		
106						D	D		200	
226									E4	
476										
107										N



Size		0201			_ 0	0402	_				06	03						0805						12	06		_ [- 1	210		
Code	4V	6.3V	10V	6.3V	10V	16V	25V	35V	6.3V	10V	16V	25V	35V	50V	4V	6.3V	10V	16V	25V	35V	50V	6.3V	10V	16V	25V	35V	50V	6.3V	10V	16V	25V	35V	50\
105	S1	S1	S2	0	0	0	0	03	B5	B5	B5	B5	B5	B5		D	D	D	D	D	D			Е	E		E	F	F	F	F		F
225	S	S2		0	0	02	02	0	B5	B5	B5	B5	B5	ВЗ		D	D	D	D	D	D		E	Е	E5		E	F	F	F	F	()	F
475	S3	S3		01	01	0			B5	B5	B4	B4	B3			D	D	D	D	D	D		E5	E5	E5		E	F	F	F	F		F
106				02	02				B5	B4	B4	B3	В			D	D	D	D	D	D	E5	E5	E5	E5	E5	E5	F	F1	F1	F1	F1/M	F1/
106 226 476 107 227				04	04				B3	В3						D	D	D	D			E5	E5	E5	E5			M	M	M	M		
476									B3	7					D	D	D					E5	E5	E4				M	M	M	N		
107															D	D						E4		-				N	N	Ν			

[■] Other dimensions, capacitance values and voltages ratings are available on request. Please contact Holy Stone.

Thickness Dimensions

Symbol Code	S	S1	S2	S3	0	01	02	03	04
Thickness (mm)	0.3±0.03	0.3±0.05	0.3±0.09	0.5±0.05	0.5±0.05	0.5±0.15	0.5±0.20	0.5±0.10	0.8+0/-0.15
Symbol Code	В	В3	B4	B5	С	D	E	E4	E5

Symbol Code	F	F1	G	M	N
Thickness (mm)	2.0±0.2	2.0±0.2	2.4±0.2	2.5±0.2	2.5±0.3



Multilayer Ceramic Capacitors 6.3V ~ 100V High Capacitance(2.2uF and above)

HCD Series



Holy Stone high capacitance products are designed and manufactured to meet the general requirements of international standards. The HCD product offering is ideally suited for server power, USB PD charger, micro PV inverter, Power over Ethernet, and power tools applications where effective capacitance at working voltage is critical to circuit design.

Features

- ±15% Temperature Coefficient from -55° C to +125° C
- □ Low Bias characteristics
- □ Small Size & Large Capacity Capacitor

Applications

- ☐ Server Power (12V and 48V)
- ☐ USB 3.1 PD Chargers (20V, 28V, 36V and 48V)
- ☐ Micro PV Inverter
- □ Power Supplies
- ☐ General Telecommunications Equipment / PoE (48V)
- Power Tools (12V, 18V, 24V, 28V and 48V)

Summary of Specifications

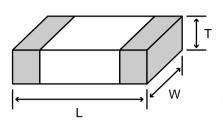
Operation Temperature	-55 °C to +125 °C				
Rated Voltage	6.3Vdc to 100Vdc				
Temperature Coefficient	X7R : ±15% at -55 °C to +125 °C				
Capacitance Range	2.2uF~22uF ,other capacitance values available upon request				
Dissipation Factor	2.5% max. at 1KHz 25 °C				
Insulation Resistance	V≤10V 100/CΩ ; V>10V 500/CΩ (C in Farads)				
Dielectric Withstanding	150%-200% of the rated voltage from 1 to 5 seconds. (Rated voltage ≤ 100V)				
Capacitance Tolerance	± 10%, ± 20%				
Aging	typically 1%				

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Special Requirement	Suffix
HCD: Low Bias MLCC	Ex.: 1206 1210	Ex.: X:X7R	Ex.: 225 : 22x10 ⁵ 335 : 33x10 ⁵ 475 : 47x10 ⁵ 106 : 10x10 ⁶	Ex.: K:+/-10% M:+/-20%	Ex.: 016 : 16Vdc 025 : 25Vdc 035 : 35Vdc 050 : 50Vdc 101: 100Vdc	Ex.: T: T&R 7" R: T&R 13"	Ex.: X: Polymer Termination (Super Term)	Y



Unit: mm

Dimensions



SIZE	L	w	T	Dimension Code
1206	3.20± 0.30	1.60± 0.20	1.60± 0.20	E2
1206	3.20± 0.40	1.60± 0.30	1.60± 0.30	E4
1210	3.20± 0.30	2.50± 0.20	2.00± 0.20	F
1210	3.20± 0.40	2.50± 0.30	2.50± 0.30	G2

Capacitance Range (preferred values)

Temperature	-	a delinion	11			C	apacita	ance R	ange (p	oF)			
Characteristic	Size	Rated Voltage	\$	224	33	474	88	105	225	335	475	106	226
	1206	10V										E4	
		16V										E4	.I
		25V					1			E		E4	
		35V				5						E4	1
X7R		50V									E2		
		100V			UE:				E2	E2			
		25V											G
	1210	50V										G2	
		100V									F		



Multilayer Ceramic Capacitors 16V ~ 100V High Capacitance

HCE Series



Holy Stone high capacitance products are designed and manufactured to meet the general requirements of international standards. The HCE product offering is ideally suited for LED driver, lighting, power adapter and USB PD charger applications where effective capacitance at working voltage is critical to circuit design.

Features

- → +/-15% Temperature Coefficient from -55° C to +125° C
- □ Excellent DC Bias characteristics
- ☐ Small Size & Large Capacity Capacitor

Applications

- ☐ LED Drivers
- □ Power Adapters/USB PD Chargers
- ☐ Lighting
- Power Supplies
- ☐ General telecommunications equipment

Summary of Specifications

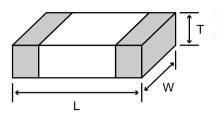
Operation Temperature	-55 °C to +125 °C
Rated Voltage	16Vdc to 100Vdc
Temperature Coefficient	X7R : ±15% at -55 °C to +125 °C
Capacitance Range	1.0uF~22uF ,other capacitance values available upon request
Dissipation Factor	5% when V=100V; 10% when V=6.3V-50V (at 1KHz 25 °C)
Insulation Resistance	V≤10V 100/CΩ ; V>10V 500/CΩ (C in Farads)
Dielectric Withstanding	200% of the rated voltage from 1 to 5 seconds. (Rated voltage ≤ 100V)
Capacitance Tolerance	± 10%, ± 20%
Aging	typically 1%

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Special Requirement	Suffix Code
HCE: Excellent DC Bias MLCC	Ex.: 0603 0805 1206 1210	Ex.: X : X7R	Ex.: 105 : 10x10 ⁵ 106 : 10x10 ⁶ 226 : 22x10 ⁶	Ex.: K:+/-10% M:+/-20%	Ex.: 016 : 16Vdc 025 : 25Vdc 035 : 35Vdc 050 : 50Vdc 101: 100Vdc	Ex.: T: T&R 7" R: T&R 13"	Ex.: X: Polymer Termination (Super Term)	Y



Unit: mm

Dimensions



SIZE	L = K-1	W	T	Dimension Code
0603	1.60±0.15	0.80±0.15	0.85±0.15	B1
0805	2.00±0.20	2.00±0.20 1.25±0.20 1		D
0805	2.00+0.45/-0.02	1.25±0.25	1.25±0.25	D1
1206	3.20±0.30	1.60±0.20	1.60±0.20	E1
1210	3,20±0.30	2.50±0.20	2.00±0.20	F
1210	3.20±0.40	2.50±0.30	2.50±0.30	G2

Capacitance Range (preferred values)

Temperature		Rated				Ca	pacita	nce R	ange (pF)			
Characteristic	Size	Voltage	104	224	334	474	684	105	225	335	475	106	226
		16V						B1					
	0603	25V						B1		-	7.7		
0805		35V						B1					
	16V							7.1		D1	- 1/		
	25V									D1			
	0805	35V	-								D1		
X7R		50V						D	D				
A/K		100V						D					
- F	1206	50V									E1		
	1200	100V						E1	E1				
		16V			ŀΞi					1			G
1 4 111	1210	25V											G
	1210	50V									-	G2	
		100V							F		F		



Multilayer Ceramic Capacitors
6.3V ~ 25V High Capacitance (0.1uF and above)

HCH Series



Holy Stone offer high capacitance product line ranging from compact (0.6x0.3mm) to large (3.2x1.6mm) for a variety of temperature characteristics, packages and rated voltages.

Features

- ☐ Materials with general purpose dielectric for Ceramic Capacitors
- ☐ Small size & high capacitance values
- ☐ 125°C max, special temperature characteristics

Applications

- ☐ General and specialized applications
- □ 5G Base Station

Summary of Specifications

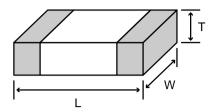
Operation Temperature	X7R:-55 °C to +125 °C			
Rated Voltage	6.3Vdc to 25Vdc			
Temperature Coefficient	X7R: ±15% at -55 °C to +125 °C (Not EIA) Range of capacitance change rate is specified with 50% of rated voltage.			
Dissipation Factor	10% max. at 1KHz 25°C			
Insulation Resistance	V≤10V 100/CΩ ; V>10V 500/CΩ (C in Farads)			
Dielectric Withstanding	250% of the rated voltage from 1 to 5 seconds. (Rated voltage ≤ 25V)			
Capacitance Tolerance	± 10% , ± 20%			
Aging	typically 1%			

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Special Requirement	Suffix
HCH: High Capacitance MLCC	Ex.: 0201 0402 0603 0805 1206	Ex.: X : X7R	Ex.: 104:10x10 ⁴ 105:10x10 ⁵ 106:10x10 ⁶ 226:22x10 ⁶	Ex.: K:+/-10% M:+/-20%	Ex.: 007 : 6.3Vdc 010 : 10Vdc 016 : 16Vdc 025 : 25Vdc	Ex.: T: T&R 7" R: T&R 13"	Ex.: X: Polymer Termination (Super Term)	Y

HCH Series – 125°C High Capacitance Capacitors



Dimensions



				Unit : mm
SIZE	L	w	Ţ	Dimension Code
0201	0.60± 0.05	0.60± 0.05	0.30± 0.05	S1
0201	0.60± 0.09	0.60± 0.09	0.30± 0.09	S2
0402	1.00± 0.20	0.50± 0.20	0.50± 0.20	W
0603	1.60± 0.15	0.80± 0.15	0.85± 0.15	B1
0805	2.00± 0.20	1.25± 0.20	1.25± 0.20	D -
0805	2.00+0.45/-0.20	1.25± 0.25	1.25± 0.25	D1 -
1206	3.20± 0.30	1.60± 0.20	1.60± 0.20	E1
1206	3.20± 0.40	1.60± 0.30	1.60± 0.30	E4

Capacitance Range (preferred values)

Temperature	20.0	Rated				Cap	oacita	nce R	ange	(pF)			
Characteristic	Size	Voltage	草	224	334	474	684	105	225	335	475	106	226
		6.3V	S1										
	0201	10V	S2			12			Y.E.				
		16V				Œ							
		6.3V						W					
	0402	10V		1				W					
		16V											
	6.00	10V			B1	B1) L		С		
	0603	16V							5=				
X7R ※		25V		B1				B1	B1				
		6.3V										D	
	0005	10V				, ===	17.		111			D	Ŧ,
	0805	16V					'n,		11		D1		
		25V			=7				HE.		D1		
		6.3V					171		PEK	-			E
	1200	10V										E1	
	1206	16V										E1	
		25V							E			E1	



[High Capacitance NP0]

HCN Series



Replacement for Film Capacitor

Features

- ☐ Small size & high Capacitance
- ☐ Suitable for wave and reflow soldering
- □ Excellent characteristics and tight tolerances
- Excellent Bias, high temperature stability & low Tan δ
- ☐ Replace Film Capacitors
- ☐ RoHS compliant

Applications

□ Suitable for ADSL filter circuits, cable Modem and coupling circuits, general Telecommunication use, power (Inverter for oscillation circuit), wireless charger and audio circuit

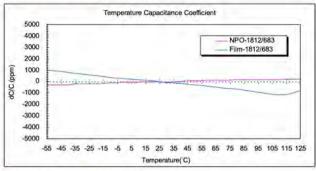
Summary of Specifications

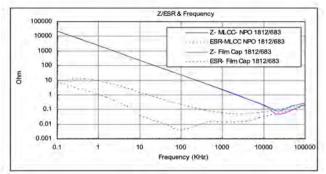
Operation Temperature	-55 °C to +125 °C
Rated Voltage	16Vdc to 50Vdc
Temperature Coefficient	NP0 : ≤ ±30ppm/ °C , -55 °C to +125 °C (EIA Class I)
Capacitance Range	1nF ~ 220nF
Dissipation Factor	Q ≥1000 at 1KHz 20 °C
Insulation Resistance	10GΩ or 500/C Ω, whichever is smaller (C in Farad)
Dielectric Strength	250% Rated Voltage for 1~ 5 seconds @ 50mA max. current
Aging	0% per decade hr.

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Thickness (mm) (Optional)	Suffix Code
C: MLCC (Multilayer Ceramic Capacitor)	Ex.: 0603 0805 1206 1210 1812	Ex.: N: NP0	Ex.: 102:10x10 ² 103:10x10 ³ 124:12x10 ⁴	Ex.: F:+/-1% G:+/-2% J:+/-5%	Ex.: 016:16Vdc 025:25Vdc 050:50Vdc	Ex.: T: T&R 7" R: T&R 13" B: Bulk	Ex: C:1.0+0.1/ -0.05 E:1.6±0.20 F:2.0±0.20	Y

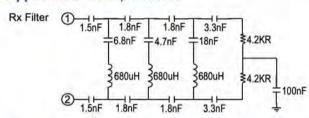


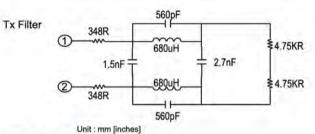
Characteristics



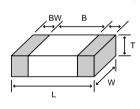


◆ Application Example Circuits





Dimensions



SIZE	L	W	T (max)	B (min)	BW (min)
0402	1.00±0.1 [.039±0.02]	0.5±0.05 [.020±0.02]	0.55 [.022]	0.30 [.012]	0.15
0603	1.60±0.10	0.80±0.10	1.00	0.40	0.15
	[.063±.004]	[.031 ±.004]	[.039]	[.016]	[.006]
0805	2.00±0.20	1.25±0.20	1.45	0.70	0.20
	[.079±.012]	[.049±.012]	[.057]	[.028]	[.008]
1206	3.20±0.30 [.126±.012]	1.60±0.20 [.063±,012]	1.80 [.071]	1.50 [.059]	0.30
1210	3.20±0.30	2.50±0.20	2.60	1.60	0.30
	[.126±.012]	[.098±.012]	[.102]	[.059]	[.012]
1812	4.60±0.30	3.20±0.30	3.00	2.50	0.30
	[.181±.012]	[.126±.012]	[.118]	[.098]	[.012]
1825	4.60±0.30	6.35±0.40	3.00	2.50	0.30
	[.181±.012]	[.250±.016]	[.118]	[.098]	[.012]
2220	5.70±0.40	5.00±0.40	3,00	3.50	0.30
	[.220±.016]	[.197±.016]	[.118]	[.137]	[.012]
2225	5.70±0.40 [.220±.016]	6.35±0.40 [.250±.016]	3.00 [.118]	3.50 [.137]	0.30

Capacitance Range

Dielectric	601	WW.											(apa	cita	nce	Ran	ige										
Characteristic	Size	Voltage	102	122	152	182	222	272	332	392	472	200	8 8	103	123	153	183	223	213	3 8	473	563	683	823	\$	124	154	774
	0402	50V	0					>					9							-	1	9 -	1			-		
		16V	В	В	В	В	В	В	В	В	В				1							100						
	0603	25V	В	В	В	В	В	В	В	в	в																	T
		50V	В	В	В	В	В	В	В	в	в																	
		16V	В	В	В	В	С	D	D	D	ם'	D i	DÉ	D	D	D	D	D										
	0805	25V	В	В	В	В	С	D	D	D	D.	D i	D C	0	D	۵	D	D										
		50V	В	В	В	В	С	D	D	D	D	D I	ם c	D	D	D	D	D	-	. (П	11
	100	16V	В	В	В	В	В	В	В	В	В	вή	В	C	D	D	D	D	ΕÜ	ΕÜΕ	EΕ							
	1206	25V	В	В	В	В	В	В	В	В	В	Вή	ВЕ	C	D	D	D	D	E)	ΕĖ	ΕŒ						- 1	-
		50V	В	В	В	В	В	В	В	В	В	В	ВЕ	C	D	D	D	D	ΕÙ	ΕĴΕ	ĒΕ							
	1	16V	C	С	С	С	C	С	C	C	C	C i	C C	C	C	C	С	D	D	DE	ĒΕ	F	F	F	F			
0.007	1210	25V	C	C	С	С	С	C	C	C)	C	C i	c] c	C	C	С	С	D	D	D E	ĒįΕ	ÇΕ	F	F	F			
NPO		50V	С	C	С	С	С	С	C	C	C	C į	C C	C	C	C	С	D	Dį.	DÈ	ijΕ	F	F	F	F			
1.77	47.5	16V	D	D	D	D	D	D	D	D	D	D i	D C	D	D	D	D	D	D.	ם ָנ	םוְכ	D	D	Е	E	F	F	F
	1812	25V	D	D	D	D	D	D	D	D	D	D ji	D C	0	D	D	D	D	D [ם [t	ם ַׁכ	D	D	E	E	F	F	F
		50V	D	D	D	D	D	D	D	D	ם	D)	D C	D	P	D	D	D	D)	D) [ם ַׁכ	D	D	Ε	Е	F	F	F
	The state of	16V	D	D	D	D	D,	D	D	D,	D,	D, I	D C	D	٥	D	D,	_,	= ,-	וְם	ם ְכ	P	D	D	D	D,		
	2220	25V	D	D	D	D	D	D.	D,	D,	D,	D,	DĮD	D	D	D	D,	D	D,	DĮI	ם ֶּׁכ	D	D	D	D.	D.		
1.0		50V	D	P	D	₽	₽,	D	D,	Φ,	D,	וְם	ם ְם	D	, D	₽	Δ,	D.	Φ,	וַם	ם, כ	, D	D	_	÷,	D	Ц	11
	.00.00	16V	D	P	D	₽	D	D,	D,	D,	D,	D į	ם ְנ	D	۰D	D	Φ,	Ε,	E,	ΕļΕ	ĻE	ĻΕ	,E	_	E			
	1825	25V	D	₽	Ρ,	Δ,	횬,	Φ,	₽,	Ρ,	₽,	١	ם ְם	D	۰	₽,	Δ,	Ε,	٤,	ΕļΕ	Ļ	ĻΕ	,Ε,	-	E			_
		50V	D		Φ,	Ρ,	Д,	Φ,	D,	Φ,		Ρ,	ם ְנ	-	-	₽,	Δ,	E,	E,	E, E	ĻE	ĻΕ	,Ε	-	E,	_	_	
	10000	16V	-	₽	_	₽,	D,	Φ,	D,	D,	₽,	D,	ם ָם	45	, -	₽,	D,	D,	Ρ.	ם, ם	ם ְנ	Ď	,D	_	Φ,	-	_	4
	2225	25V	-	. □	_	₽,	₽,	D,	D,	D,		= ,	ם ְם	-		,₽	₽,	D.	D.	ון ס	ם ְכ	P	₽,	-	D,	-	_	_
		50V	D	D	D	D	D	D	D,	D,	D,	D .	ם ָם	ם	D	D	D	D	D,	D [ם ֶׁנ	D	D	D	D	D		

Other dimensions, capacitance values and voltages ratings are available on request. Please contact Holy Stone.



[X2 and X1/Y2 Safety Certified Capacitors] SCC Series Rated up to 305Vac





The SCC series X2 and X1/Y2 rated at 250Vrms and X2 rated at 305Vrms safety capacitors are designed specifically for use in modem, facsimile, telephone and other electronic equipment. These parts are compliant to IEC60384-14, UL60950-1 and UL60384-14 standards. These capacitors are available in NP0 (C0G) and X7R dielectrics.

Features

- ☐ Small size & high capacitance
- Suitable for reflow soldering
- ☐ Surface mount
- ☐ Safety standard approval by IEC60384-14, UL 60950-1 and UL60384-14
- ☐ RoHS compliant and Lead(Pb) free option
- ☐ Certified to:

TUV R50005234, R50103496 & UL E229738 TUV R50162550 & UL E300818 & UL E229738 for Lead(Pb) free

Applications

☐ The X2 and X1/Y2 (250Vrms) and X2-(305Vrms) are specially designed for use in Modem, Facsimile, Telephone and other telecommunication equipment, electronic equipment for lighting and surge protection, EMI filtering and Isolation.

Safety Details of Specifications

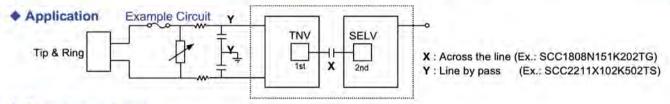
IEC 60384-14:2013+AMD1:2016	Meets the electrical requirements and certification for equipment requiring Class
EN 60384-14:2013+AMD1:2016	X1/Y2 and X2 devices.
UL 60384-14 : 2014, 2 nd Edition	Component certified for equipment requiring UL-60384-14 compliance
UL 60950-1 : 2007, 2 nd Edition	TNV/SELV isolation capacitors certified To UL 60950-1

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Class	Packaging	Special Requirement
SCC: Safety Certified MLCC	Ex.: 1808 1812 2208 2211 2220 2825	Ex N: NP0 X: X7R	Ex.: 2R0:2.0pF 100:10×10 ⁰ 471:47×10 ¹ 182:18×10 ²	Ex.: J :+/-5% K :+/-10% M :+/-20%	Ex.: 202: X2 252: X2 (305Vrms) 502: X1/Y2 602: X1/Y2 for SCC2208N, SCC2211N, SCC2220N	Ex.: T: T&R 7" R: T&R 13" B: Bulk	Ex.: S: Arc Prevention Coating X: Polymer Termination (Super Term) Z: Arc Prevention Coating Polymer Termination (Super Term) G: Lead (Pb) Free



Summary of Specifications

Rated Voltage	AC 250Vrms and AC 305Vrms
T	NP0 : < ±30ppm/ °C , -55 °C to +125 °C (EIA Class I)
Temperature Coefficient	X7R ; < ±15% , -55 °C to +125 °C (EIA Class Ⅱ)
	X1/Y2: 2.0pF ~ 4700pF
Capacitance Range	X2 – 250Vrms : 2pF ~ 56nF X2 – 305Vrms : 150pF ~ 33nF
Quality and Dissipation Factor	NP0 : Q≧1000 , X7R : D.F.≦2.5%
Climatic Category	55/125/21
Insulation Resistance	10GΩ
Voltage Proof	X Capacitor : Applied Voltage 1075Vdc(4.3Ur),1312Vdc(4.3Ur) Y Capacitor : Applied Voltage 1500Vac
Impulse	X2:2.5KV, Y2:5KV for three times
Aging	NP0: 0 % , X7R: 1.0 % per decade hr., typically



Capacitance Range

		Temperature	Rated	7 1	Capacitance Range
Class	Size	Characteristic	Voltage	Certificate	2000 2000
77.5	1808	NP0	250Vrms	TUV/UL	
X2	1808	X7R	250Vrms	TUV/UL	
7	1812	X7R	250Vrms	TUV/UL	
	1808	NP0	250Vrms	TUV/UL	
	1808	X7R	250Vrms	TUV/UL	
	1812	NP0	250Vrms	TUV/UL	
1	1812	X7R	250Vrms	TUV/UL	
VANO	2208	NP0	250Vrms	TUV/UL	
X1/Y2	2208	X7R	250Vrms	TUV/UL	
	2211	NP0	250Vrms	TUV/UL	
	2211	X7R	250Vrms	TUV/UL	
1	2220	NP0	250Vrms	TUV/UL	
	2220	X7R	250Vrms	TUV/UL	XXXXX X XXXXX
		Temperature	Rated	h oridi	Capacitance Range
Class	Size	Characteristic	Voltage	Certificated	<u> </u>
wa	2220	X7R	250Vrms	TUV/UL	
X2	2825	X7R	250Vrms	UL	

^{&#}x27;X' denotes values that have been tested to a rated voltage of 305Vac. TUV test report number 28208004 dated on May 27th, 2010.

305Vrms	4				
Class	Size	Temperature	Rated	Cartificated	Capacitance Range
Class	Size	Characteristic	Voltage	Certificated	88 4 3 3 3 3 3 3 5 4 3 5 7 2 8 8 8 4 3 4 3 3 7 2 8 8 8 8 4 3 8 8 8 8 8 8 8 8 8 8 8 8 8 8
X2	2220	X7R	305Vrms	TUV/UL	



[Low-Loss, High Frequency Capacitors]

HBC Series



Features

- ☐ Low stable ESR at high frequency
- ☐ Ultra stable NP0 performance
- ☐ Suitable for solder wave and reflow soldering
- ☐ RoHS compliant
- ☐ High peak to peak voltage capability

Applications

- ☐ High frequency pulse circuits
- ☐ Lighting ballast snubber circuits
- □ DC-DC converters
- ☐ High dv/dt rating

Summary of Specifications

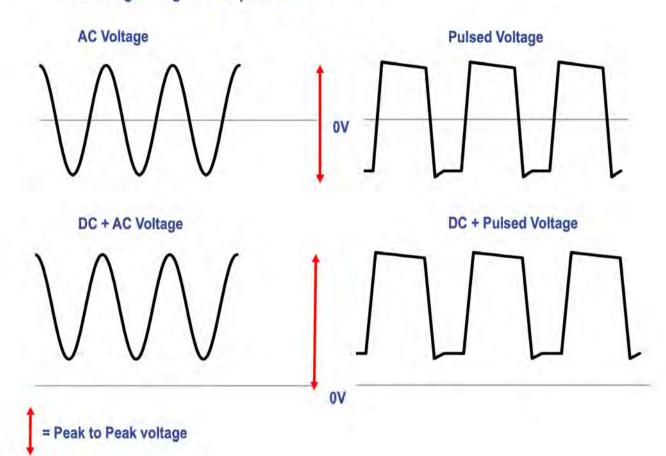
Operation Temperature	-55 °C to +125 °C	
Rated Voltage	500Vdc, 630Vdc and 1000Vdc	
Temperature Coefficient	≤ ± 30ppm at -55 °C to +125 °C	
Capacitance Range	10pF ~ 2700pF	
Dissipation Factor	0.1% max. at 1MHz 25 °C	
Insulation Resistance	10GΩ	
Dielectric Withstanding	1.5 x WVDC for 5 sec	
Capacitance Tolerance	±1%, ±2%, ±5%, ±10%	
Ageing	None	
Piezo Effects	None	
dv/dt Rating	>8KV/µ second	

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Thickness (mm) (Optional)	Suffix
HBC: Low-Loss High Frequency Capacitor	Ex.: 1206 1210	Ex.: N: NP0	Ex.: 100 : 10×10 ⁰ 101 : 10×10 ¹ 102 : 10×10 ²	Ex.: F:±1% G:±2% J:±5% K:±10%	Ex.: 501 : 500Vdc 631 : 630Vdc 102 : 1000Vdc	Ex.: T: T&R 7" R: T&R 13" B: Bulk	Ex: E:1.60±0.20 F:2.0±0.20	Y

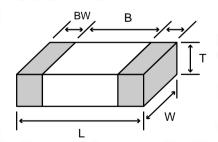


Characteristics Peak to Peak Voltage

The maximum Peak to Peak voltage, as defined below, should not exceed the DC voltage rating of the capacitor



Dimension



SIZE	L	W	T (max)	B (min)	BW (min)
1000	3.20±0.30	1.60 ± 0.2	1.80	1.50	0.30
1206	[.126±.012]	[.063±.008]	[.071]	[.059]	[.012]
1010	3.20±0.30	2.50 ± 0.2	2.60	1.60	0.30
1210	[.126±.012]	[.098±.008]	[.102]	[.063]	[.012]

Capacitance Range

57 W	Jane	155					2.			Сар	aci	tan	се	Ra	nge	•						5			9.1	
Size	Voltage	120	150	180	270	330	470	560	828	101	121	151	181	221	271	331	391	471	561	681	821	102	122	152	182	336
1206	500V							•													•					Ť
1206	630V									Г																
1206	1000V		٠,			7			1	Г																
1210	500V		٠.,		٠.				•	Т																-
1210	630V				٠.,		_	٠.,		Г								_								
1210	1000V				٠.		•	٠.,	*	Т									_	-						

[■] Other dimensions, capacitance values and voltages rating are available. Please contact Holy Stone.

Unit: mm [inch]



[Automotive Grade MLCCs]

ACC Series



Features

- ☐ AEC-Q200 & IATF16949 qualified.
- □ Suitable for harsh Automotive environments without additional qualification testing
- ☐ Available with Polymer Termination (Super Term) to prevent mechanical cracking
- ☐ High Reliability
- ☐ RoHS compliant
- □ 250Vac, X1/Y2 Safety capacitors available

Applications

- Power supplies
- ☐ Lighting
- ☐ Isolation
- □ Powertrain□ Safety equipment
- ☐ Custom applications, BMS, On board charger

Summary of Specifications

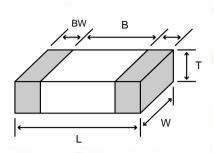
Operation Temperature	-55 °C to +125 °C	
Rated Voltage	16Vdc ~ 1000Vdc , 250Va	ac X1/Y2 Safety capacitors
Temperature Coefficient	NP0 : ≤ ± 30ppm/ °C	-55 °C to +125 °C (EIA Class I)
	X7R : ± 15%	-55 °C to +125 °C (EIA Class Ⅱ)
Capacitance Range	NP0 : 10pF ~ 47nF ; X7R	R: 330pF ~ 4.7uF
Dissipation Factor	NP0 : More than 30pF Q X7R : Range 2.5% to 109	≧1000 ; 30pF & below Q≧400+20C %
Insulation Resistance	10GΩ or 500/C Ω, whiches	ver is smaller (C in Farad)
Aging	NP0 : 0% ; X7R : 2.5% p	er decade of time
Dielectric Withstanding	V <100V	250% rated voltage
	100V ≤ V < 500V	200% rated voltage
	500V ≤ V < 1000V	150% rated voltage
	1000V ≤ V	120% rated voltage

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Thickness (mm) (Optional)	Special Requirement	Suffix
ACC : Automotive Grade Capacitors	EX: 0805 1206 1210 1812 1825 2222	EX: N:NP0 X:X7R	EX: 100:10 x 10° 221:22 x 10¹ 332:33 x 10² 473:47 x 10³ 684:68 x 10⁴	Ex: J: +/- 5% K: +/-10% M: +/-20%	EX: 025: 25Vdc 050: 50Vdc 101: 100Vdc 251: 250Vdc 501: 500Vdc 102: 1000Vdc 202: 2000Vdc	EX: T:T&R 7" R:T&R 13" B:Bulk	Ex: D: 1.25±0.20 E: 1.60±0.20 I: 3.2±0.20	EX: X: Polymer Termination (Super Term) O: Arc Prevention Coating Z: Coating & Polymer Termination	Y



Dimensions

Unit: mm



SIZE	1	W	T (max)	B (min)	BW (min)
0402	1.00±0.05	0.5±0.05	0.55	0.30	0.15
0805	2.00 +0.45/-0.20	1.25 +0.25/-0.20	1.25±0.25	0.70	0.20
1206	3.20.±0.30	1.60±0.20	1.6±0.30	1.50	0.30
1210	3.20±0.30	2.50±0.20	2.60	1.60	0.30
1812	4.60±0.30	3.20±0.30	3.00	2.50	0.30
1825	4.60±0.30	6.35±0.40	3.00	2.50	0.30
2220	5.70±0.40	5.00±0.40	3.00	3.50	0.30

♦ Capacitance Range: X7R 16V~3000V

	Voltage																Ca	pa	cita	nce	Ra	ang	je (F)	- A	CC	Sei	ies -	X7	R														
Size		151	331	391	471	561	100	170	102	152	182	222	272	332	392	714	200	822	103	123	153	183	223	333	393	473	563	683	104	124	154	184	224	334	394	474	564	684	824	105	100	335	475	106
0402	50V							I	1						I	1	Ī	4							I				0			1		T										\perp
	16V		В	В	В	В	В	3 1	ВЕ	3 B	В	В	В	В	В	3 E	B E	3 B	В	В	В	В	C	0 0	C	C	C	C	C	D	D	D	D [0 0	D	D	D	D	D	D	ם כ			1
	25V		В	В	В	В	В	В	ВЕ	3 B	В	В	В	В	В	3 E	3 E	3 B	В	В	В	В	С	0 0	C	C	C	C	C	D	D	D	D E	0 0	D	D	D	D	D	D	D [ΙÌ	
0805	50V		В	В	В	В	В	В	ВЕ	В	В	В	В	В	В	3 E	3 E	3 B	В	В	В	В	С	0 0	C	C	C	C	C	D	D	D	D E) [D	D	D	D	D	D			l t	
	100V		В	В	В	В	В	В	ВЕ	В	В	В	В	В	В	3 E	3 E	3 B	В	В	В	В	С	0 0	C	C	C	C	C	D	D	D	D [) [D	D	D	D	D	D				
	250V		В	В	В	В	В	В	ВЕ	В	В	В	В	В	В	3 E	3 E	3 E	В	В	В	В	C	0		ı							1											
	25V		В	В	В	В	В	В	ВЕ	3 B	В	В	В	В	В	3 E	3 E	3 B	В	В	В	В	В	3 E	3 B	C	C	DI	E	E	Ė	E	E E	E	E	E	E	E	E	E	EE	E	Е	1
	50V		В	В	В	В	В	В	ВЕ	3 B	В	В	В	В	BI	3 E	3 E	3 B	В	В	В	В	В	3 E	3 B	C	C	D I	E	E	Ε	E	E E	E	E	E	E	E	E	E	E E			
1206	100V		В	В	В	В	В	B	ВЕ	В	В	В	В	В	В	3 E	3 E	3 B	В	В	В	В	В	3 B	В	C	C	D I	E	E	Е	E	E E	E	E	Ε	E	E	E	E	ı			
1200	250V		В	В	В	В	В	В	ВЕ	3 B	В	В	В	В	ВЕ	3 E	3 E	3 B	В	В	В	В	В	3 B	3 B	C	C	D I	E					1										
	630V			1		T	1		C	C	Ç	C	Ç	С	C	0	C (C	D					T	F	П						- [Ţ		L		Ц				Ţ	L	F	T
	1KV					T			D E	ם	D	D	E	E	ΕŒ		ı	I								I															T			
	25V				I		П	I								i	1		Г																					F	FF	F	F	F G
1210	50V			П	J		ij.	I	I	I				Ī	Ĩ	Ī										П						4	1							F I	FF	F	F	
1210	1KV			Т	I	T			C (C	C	С	D	D	D I) I	ו כ) E	E					Т	П	П				П														
	2KV	C															T													П				1		1				j.				
1812	1KV		D	D	D	D I	D I	D I	D [D	D	D	D	D	D I	O E	E (E	E		-			I			-			Г						Į,					I			
1812	2KV	D						0 1		0 0										F						I							1	1		H								
2220	2KV			1			1		FF	F	F	F	F	F	F	= 1	F	F	F	G	G	G	G											1										
2220	3KV	D D	D	D	D	D I	D													F				Ī	Г					Г	ΠÌ	П	ī	1				П	T		Т	1	П	

Note : Only Polymer Termination (Super Term) Available

Unavailable for Polymer Termination (Super Term)

Capacitors must be coated.

Thickness Dimensions

1	Symbol Code	В	С	D	E	F	G	Н	
	Thickness(mm)	0.85±0.15	1.0+0.1/-0.05	1.25±0.20	1.6±0.2	2.0±0.2	2.4±0.2	2.8±0.2	3.2±0.2

ACC Series - Automotive Grade Capacitors



◆ Capacitance Range: NPO 25V~1000KV

5.4	Voltage																		C	ар	aci	tar	nce	Ra	ang	e (oF)		ACC	c s	eri	es-	NP	0	П			П												
Size	(v)	100	120	150	180	200	220	270	300	330	390	470	260	680	820	101	121	151	181	221	271	331	391	471	561	681	821	102	122	152	182	222	272	332	392	472	562	682	778	103	123	153	183	577	333	393	563	683	823	104
111	25	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	С	D	D	D	D	D	П	1	П	Ī		П		ī	T	T	Ī		
0005	50	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	С	D	D	D	D	D													
0805	100	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	С	D	D	D	D	D		T							T		T		T
	250	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	С	D	D	D						Ī					Ţ				1
	25				В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	C	D	D	D	D	E	E	T			
	50				В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	в	C	D	D	D	D	E	Ε				
	100				В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	С	C	D	D	Е	Е			-1	-	Н			1		
1206	250				В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	С	С	D	D	E	Ε				1		1	T			i
	500		П		В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	С	C	C	D	D	Е	E						ij			9	П	1		Ī			T		Т	T	T		Ī
	630		П		В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	С	C	C	D	D	E	E	Н			П							1		Ī		П	1	П	T				П
	1000				В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	В	С	C	C	D	D	E	E						Ī					1		1		1			1	T			Ī
1210	1000																			Ĺ		D	D	D															1		Ì			Ì						
1812	1000																				Г		Г	Г				Ε	E	F	F	F	F	F	F	F	G	G	1		Ì				Ť					
1825	500					П				П	П		П											Ī	П									ı		П	1			D	Е	E	E	F	F	F	G			T
	500																					D	D	D	D	D	D	D	D	D	D	D	D	D	D	D	D	D	Е	E	F	F	G	G	1	T		T		Ī
	630							П														D	D	D	D	D	D	D	D	D	D	D	D	D	D	D	D	D	E	E	F	F	G	G	1	1	T	Ī		
2220	1000							П											Г			D	D	D	D	D	D	D	D	D	D	D	D	D	D	D	D	D	E	E	F	F	G	G	1	T		T		Ī
	2000		П	1		П				П			П				П		П		î		D	D	D	D	D	D	D	D	D	D	E	E	E	F	F	G	1	T	T			Ī	П	T	T	1		T

Note :

Available with Standard or Polymer Termination (Super Term)

Only Polymer Termination (Super Term) Available

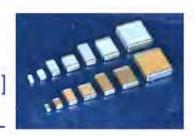
Thickness Dimensions

Symbol Code	В	С	D	E	F	G	Н	4.4
Thickness(mm)	0.85±0.15	1.0+0.1/-0.05	1.25±0.20	1.6±0.2	2.0±0.2	2.4±0.2	2.8±0.2	3.2±0.2

- Other dimensions, capacitance values and voltage ratings are available on request. Please contact Holy Stone.
- Specifications & Test Conditions for 0603 and larger sizes, please see P56~P60.
- Specifications & Test Conditions for 0402, please contact Holy Stone.



[Automotive Grade - X1/Y2 and X2 Safety Certified Capacitors] Rated Voltage 250Vac



Features

- □ Suitable for harsh Automotive environments without additional qualification testing
- Available with Polymer Termination (Super Term) to prevent mechanical cracking
- ☐ High Reliability
- □ IATF16949 & AEC-Q200 qualified
- ☐ RoHS compliant
- ☐ 250Vac, X1/Y2 and X2 Safety Capacitors available

Safety Details of Specifications

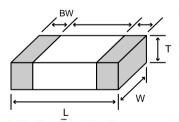
Applications

- ☐ CAN-Bus Isolation, On Board Charger
- □ Safety equipment
- ☐ Customized application

How To Order:

SCC	2220	X	472	K	502	Т	X	A
Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Class	Packaging	Special Requirement	Suffix Code
SCC: Safety Approved MLCC	Ex.: 1808 2208 2220	Ex.: X: X7R	Ex.: 100 : 10 x 10 ⁰ 221 : 22 x 10 ¹ 332 : 33 x 10 ²	Ex.: J:+/-5% K:+/-10% M:+/-20%	Ex.: 202: X2 502: X1/Y2	Ex.: T: T&R 7" R: T&R 13" B: Bulk	Ex.: X: Polymer Termination (Super Term)	EX: A : Automotive Grade

Dimension



SIZE	L	W	T (max)	BW (min)
1808	4.70±0.30	2.00±0.20	2.2	0.2
2208	5.80±0.50	2.00±0.20	2.2	0.3
2220	5.80±0.50	5.00±0.40	3	0.3

Capacitance Range

	Voltage						C	apa	cita	inc	e Ra	nge	(p	F) - A	CC	Se	erie	s - X	7R							
Size	(v)	120 120 190	330	420	560	820	101	131	181	201	221	301	361	391	471	511	621	681	821	122	152	202	272	472	682	103
1808	X2								D D		D D		0	D	D		D 📄	D	D	D						
2208	X1/Y2	4111	EE	EE	E	ΕE	FF		FF	F	FF	F	F	F	F		F	F	F	F						1-1-1-
2220	X1/Y2	1 -1 -6 1		111		-	FF	F		Ĭ ii	FF			F	F		F	F	F	F	F	F	FG	Н		

 Symbol Code
 D
 E
 F
 G
 H
 I

 Thickness(mm)
 1.25±0.20
 1.6±0.2
 2.0±0.2
 2.4±0.2
 2.8±0.2
 3.2±0.2

Other dimensions, capacitance values and voltage ratings are available on request. Please contact Holy Stone.

Unit: mm



[250V to 630V High Voltage & Low DC Bias]

HCP Series



Holy Stone high voltage products are designed and manufactured to meet the general requirements of international standards. The X7P product offering is ideally suited for LED driver, lighting, power adapter and USB charger applications where effective capacitance at working voltage is critical to circuit design.

Features

- □ +/-10% Temperature Coefficient from -55° C to +125° C
- ☐ Low DC Bias characteristics
- ☐ Competitive price compared to X7T dielectric
- ☐ 1206, 1210, 1812, 2220 sizes. Other sizes and dielectric available upon request

Applications

- □ LED Drivers
- □ Power Adapters/USB Chargers
- ☐ Lighting
- ☐ Power Supplies
- ☐ General telecommunications equipment

Summary of Specifications

Operation Temperature	-55 °C ~ +125 °C
Rated Voltage	250Vdc, 400Vdc , 450Vdc and 630Vdc
Temperature Coefficient	± 10% at -55 °C ~ +125 °C
Capacitance Range	10nF ~0.68uF, other capacitance values available upon request
Dissipation Factor	0.8% max. at 1KHz 25 °C
Insulation Resistance	10GΩ or 500/CΩ, whichever is smaller
Dielectric Withstanding	1.5 x WVDC for 5 sec
Capacitance Tolerance	5% , 10% , 20%

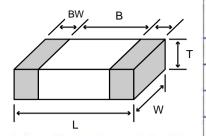
Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Thickness (mm) (Optional)	Special Requirement	Suffix
C: MLCC (Multilayer Ceramic Capacitor)	Ex: 1206 1210 1812 2220	Ex: P: X7P	Ex: 103: 10 x 10 ³ 104: 10 x 10 ⁴ 224: 22 x 10 ⁴	Ex: J :+/- 5% K :+/-10% M:+/-20%	Ex: 251: 250Vdc 401: 400Vdc 451: 450Vdc 631: 630Vdc	Ex: T: T&R 7" R: T&R 13" B: Bulk	Ex: D: 1,25±0.20 E: 1.60±0.20	Ex: O: Arc Prevention Coating X: Polymer Termination (Super Term) Z: Arc coating and Polymer Termination	Y

HCP Series – Low DC Bias Capacitors



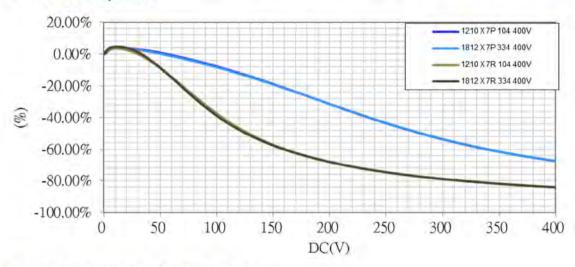
Unit: mm [inches]

Dimensions



SIZE	L	w	T (max)	B (min)	BW (min)			
1206	3.20±0.30	1.60±0.20	1.80	1.50	0.30			
	[.126±.012]	[.126±.012]	[.071]	[.059]	[.012]			
		2.50±0.20	2.60	1.60	0.30			
		[.126±.012]	[.102]	[.059]	[.012]			
1812	4.60±0.30	3.20±0.30	3.00	2.50	0.30			
	[.181±.012]	[.126±.012]	[.118]	[.098]	[.012]			
2220 5.7±0.40 5.00±0.40 [.220±.016]		3.00	3.50	0.30				
		[.118]	[.137]	[.012]				

DC Bias Comparison



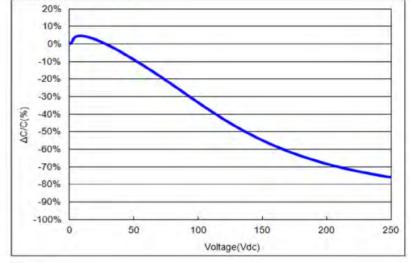
Capacitance Range (preferred values)

Temperature Characteristic	Voltage (V)	Size	Capacitance Range																		
			103	123	153	183	223	273	333	393	473	563	683	823	104	154	224	334	394	474	684
	250V	1206	В		В		В		В	В	С		С		D		Е				
		1210							С		С		С	Ė	С		F	F	F		
	400V	1206	В		В		С	H	D	D	Έ		E		Е						
X7P 45		1210				17			С		D		D		E	F	Н	F			
		1812															F	F			
		2220				H														F	F
		1206				1							Е	+	Е						
	450V	1210															F				
		1812	-				_										F	F	Ē	G	
	0001	1206	В		В		С		D	D	Ε		E								
	630V	1210							С		D		D		E/F	F/G					

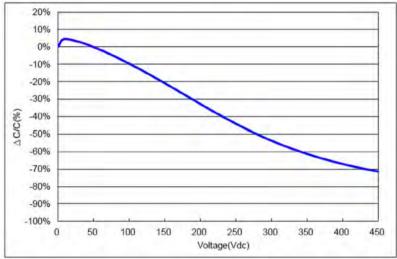
[■] Other dimensions, capacitance values and voltage ratings are available on request. Please contact Holy Stone.



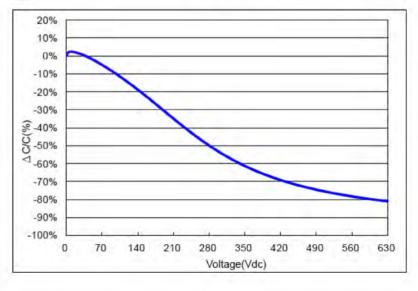
DC Bias Characteristics



C1206P224K251T DC Bias Characteristics (typical)



C1206P473K451T DC Bias Characteristics (typical)



C1210P104K631T DC Bias Characteristics (typical)



Multilayer Ceramic Capacitors

[250V to 630V High Voltage & Low DC Bias]

HCT Series



Holy Stone high voltage products are designed and manufactured to meet the general requirements of international standards. The X7T product offering is ideally suited for LED driver, lighting, power adapter and USB charger applications where effective capacitance at working voltage is critical to circuit design.

Features

- □ +22/-33% Temperature Coefficient from -55° C to +125° C
- □ Low DC Bias characteristics
- 1206, 1210, 1812, 2220 sizes. Other sizes and dielectric available upon request

Applications

- ☐ LED Drivers
- □ Power Adapters/USB Chargers
- □ Lighting
- □ Power Supplies
- ☐ General telecommunications equipment

Summary of Specifications

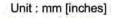
Operation Temperature	-55 °C ~ +125 °C
Rated Voltage	250Vdc, 400Vdc, 450Vdc and 630Vdc
Temperature Coefficient	+22/-33% at -55 °C ~ +125 °C
Capacitance Range	10nF ~ 0.68uF, other capacitance values available upon request
Dissipation Factor	2.5% max. at 1KHz 25 °C
Insulation Resistance	10GΩ or 500/CΩ, whichever is smaller
Dielectric Withstanding	1.5 x WVDC for 5 sec.
Capacitance Tolerance	10%, 20%

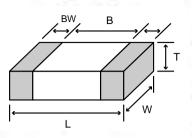
How To Order

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Thickness (mm) (Optional)	Special Requirement	Suffix Code
C: MLCC (Multilayer Ceramic Capacitor)	Ex: 1206 1210 1812 2220	Ex: T: X7T	Ex: 103: 10 x 10 ³ 104: 10 x 10 ⁴ 224: 22 x 10 ⁴	Ex: J:+/- 5% K:+/-10% M:+/-20%	Ex: 251: 250Vdc 401: 400Vdc 451: 450Vdc 631: 630Vdc	Ex: T: T&R 7" R: T&R 13" B: Bulk	Ex: D: 1,25±0,20 E: 1.60±0.20	Ex: O: Arc Prevention Coating X: Polymer Termination (Super Term) Z: Arc coating and Polymer Termination	Y



Dimensions





SIZE	L	w	T (max)	B (min)	BW (min)
1206	3.20±0.30	1.60±0.20	1.80	1.50	0.30
	[.126±.012]	[.126±.012]	[.071]	[.059]	[.012]
1210	3.20±0.30	2.50±0.20	2.60	1.60	0.30
	[.126±.012]	[.126±.012]	[.102]	[.059]	[.012]
1812	4.60±0.30	3.20±0.30	3.00	2.50	0.30
	[.181±.012]	[.126±.012]	[.118]	[.098]	[.012]
2220	5.7±0.40	5.00±0.40	3.00	3.50	0.30
	[.220±.016]	[.197±.016]	[.118]	[.137]	[.012]

Capacitance Range (preferred values)

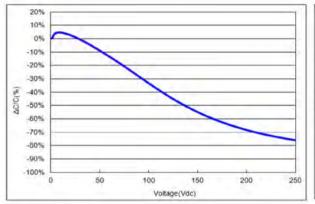
Temperature	Voltage	105								Ca	paci	tance	Ran	ge							
Characteristic	(V)	Size	103	123	153	183	223	273	333	393	473	563	683	823	104	154	224	334	394	474	684
	2501/	1206	В		В		В		В	В	С		Ç		D		E				
	250V	1210							С		C		С	-1	С		F	F	F		
		1206	В		В		С		D	D	E		Е		E						
	400V	1210							С		D		D	Ы	E	F		F			
		1812														7	F	F			
X7T		2220																		F	F
		1206											E		E					1	
	450V	1210															F				
	630V -	1812															F	F		G	
		1206	В		В		С		D	D	E		E		M						
		1210							С		D		D		E/F	F/G					

■ Other dimensions, capacitance values and voltage ratings are available on request. Please contact Holy Stone.

Symbol Code	0	Α	В	С	D	E	F	G	H	
Thickness(mm)	0.5±0.05	0.6±0.1	0.85±0.15	1.0+0.1/-0.05	1.25±0.20	1.6±0.2	2.0±0.2	2.4±0.2	2.8±0.2	3.2±0.2



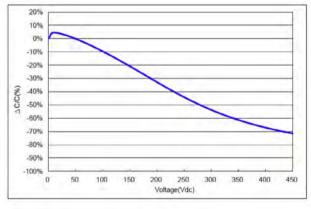
♦ DC Bias Characteristics

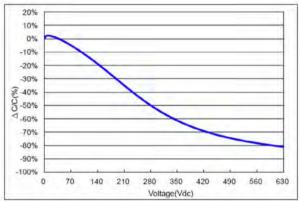


20% 10% 0% -10% -20% -30% % 40% 0 -50% -60% 70% -80% -90% -100% 0 100 200 400 Voltage(Vdc)

C1206T224K251T DC Bias Characteristics (typical)

C1206T104K401T DC Bias Characteristics (typical)





C1206T473K451T DC Bias Characteristics (typical)

C1210T104K631T DC Bias Characteristics (typical)



Multilayer Ceramic Capacitors

[Large Size Capacitors]

LCC Series



Features

- Optimized internal designs offers the highest voltage rating (up to 8KVdc)
- □ Capacitance range from 100pF to 18uF and sizes from 2520 to 3640
- Available with proprietary surface coating for arc prevention
- □ Available with flexible termination (Super Term) to minimize the effects of mechanical stress
- □ RoHS compliant

Applications

- □ Voltage Multipliers
- ☐ Power Supplies
- □ DC-DC Converters
- □ Surge protection
- ☐ Industrial control circuits
- ☐ Isolation
- ☐ Ballast
- ☐ Snubber
- ☐ Custom applications

Summary of Specifications

Operation Temperature	-55 °C ~ +125 °C
Rated Voltage	50Vdc ~ 8KVdc
Temperature Coefficient	NP0 : ≤ ± 30ppm/ °C , -55 ~ +125 °C (EIA Class I)
	X7R : ≤ ± 15% , -55 ~ +125 °C (EIA Class Ⅱ)
Capacitance Range	NP0:68pF~220nF , X7R:1000pF~18uF
Dissipation Factor	NP0 : Q≧1000 , X7R : 2.5% max.
Insulation Resistance	10GΩ or 500/CΩ, whichever is smaller (C in Farad)
Aging	NP0:0%, X7R:2.5 % per decade of time
Dielectric Strength	V ≤ 500V : 200% Rated Voltage
	500V ≤ V < 1000V : 150% Rated Voltage
	V≥1000V : 120% Rated Voltage

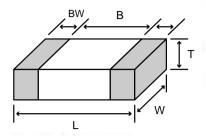
How To Order

Product Code	Chip Size	Dielectric	Capacitance Unit : pF	Tolerance	Rated Voltage	Packaging	Termination		Special Requirement	Suffin
Ceramic	Ex.: 2520 3530 3640	Ex.: N : NP0 X : X7R	Ex.: 100 : 10×10° 471 : 47×10° 102 : 10×10°	K:+/-10%	Ex.: 050:50Vdc 251:250Vdc 102:1000Vdc 202:2000Vdc		Ex: N: Ni Barrier / Sn Plate	Ex.: S: Standard Electrical	Ex.: O: Arc Prevention Coating X: Polymer Termination (Super Term) Z: Coating & Polymer Termination	Y



Dimension

Unit: mm [inches]



SIZE	L	W	T (max)	B (min)	BW (min)
2520	6,35±0.50	5.00±0.50	3.2	4.0	0.3
	[.25±.020]	[.20±.020]	[.126]	[.157]	[.012]
3330	8.4±0.50	7.6 ±0.50	4.0	4.0	0.3
	[0.33±0.2]	[0.30±0.2]	[.157]	[.157]	[.012]
3530	8.90±0.50	7.60±0.50	5.0	5.5	0.3
	[.35±.020]	[.30±.020]	[.200]	[.217]	[.012]
3640	9.20±0.50 [.36±.020]	10.20±0.50 [.40±.020]	5.0 [.200]	6.0 [.236]	0.3 [.012]

Capacitance Range

01	Districts				Cap	acitance (pF) maxim	num			
Size	Dielectric	50V	100V	250V	500V	1KV	2KV	зку	4KV	5KV	8KV
0000	NPO	823	683	563	473	103	392	222	102	471	101
2520	X7R	685	685	395	474	334	563	473	822	822	152
2220	NP0	224	184	104	683	333	823	392	292	821	251
3330	X7R	126	126	825	105	824	124	823	183	183	332
2520	NP0	224	184	104	823	473	103	472	332	102	251
3530	X7R	126	126	825	105	824	124	823	183	183	332
2010	NPO	224	184	104	823	473	123	562	392	122	561
3640	X7R	186	186	106	125	105	184	104	273	273	392

- All values are capacitance EIA codes.
- Other dimensions, capacitance values and voltages rating are available. Please contact Holy Stone.

Soldering And Handling Precautions:

Large ceramic capacitors are more prone to thermal and mechanical cracks. To minimize mechanical cracks, capacitors have to be handled carefully in the original waffle pack container, carrier tape or other suitable container. Care must be taken that these capacitors do not come into contact with each other which can cause chip outs, cracks or other mechanical damage.

The recommended method for soldering large chips is reflow soldering. Wave soldering and manual soldering with Iron is not recommended. Ceramic capacitors must be preheated with less than 2°C/second rate to about 50°C below the reflow temperature. Any sudden increase or decrease in temperature more than the recommended rate, during soldering, may cause internal thermal cracks.

Options:

- · Holy Stone offers polymer termination (Super Term) for very large chips to minimize mechanical cracks due to board flexing.
- . To minimize the potential for surface arcing in higher voltage applications, IHHEC offers the option of a proprietary surface coating.



Multilayer Ceramic Capacitors [Stacked Capacitors]

SMC Series

MLCC Design, Suitable for Switchmode Power Supply Filters



Features

- Stacked design offers the high capacitance similar to Tantalum but with extremely low ESR advantage.
- 'J', 'L' and 'N' Leaded configuration provide mechanical and thermal stress relief.
- □ Capacitance values up to 44µF. Voltage from 50V to 1KV.
- ☐ Available in NP0 and X7R dielectrics .
- ☐ HIREL screening available.
- □ RoHS compliant.

Applications

- □ Power supplies
- □ DC-DC converters
- □ Surge protection
- ☐ Industrial control circuits
- ☐ Snubbers
- ☐ Filtering, smoothing, and decoupling application
- ☐ HIREL applications
- □ Custom applications

Summary of Specifications

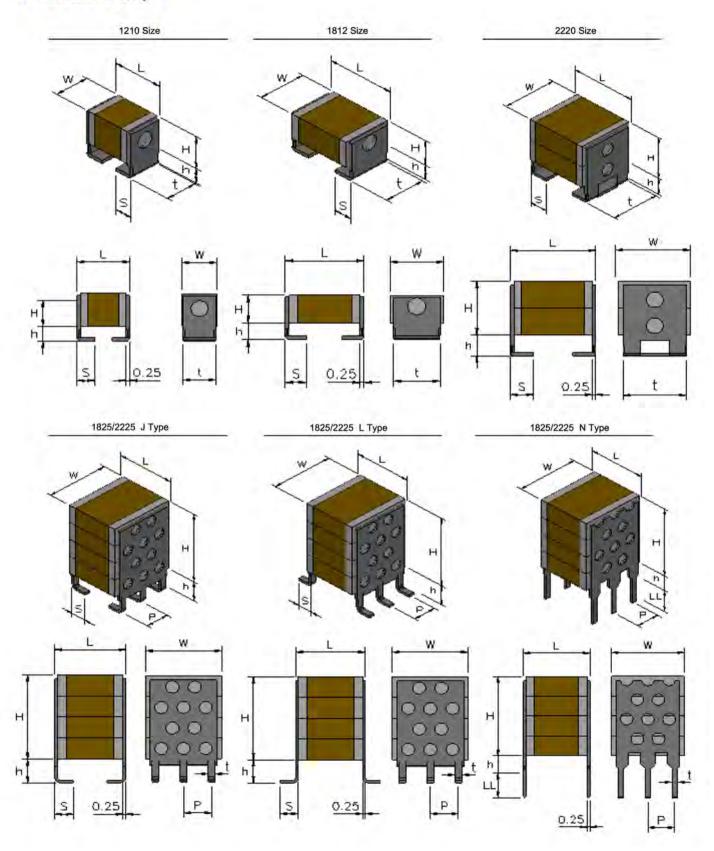
Operating Temperature	-55 to +125 ℃				
Rated Voltage	50Vdc to 1000Vdc				
Temperature Coefficient of	NPO : ≤ ± 30ppm/ °C , -55 to +125 °C (EIA Class I)				
Capacitance	X7R : ≤ ± 15% , -55 to +125 °C (EIA Class Ⅱ)				
Capacitance Range	NPO: 2.2 nF to 550nF / X7R : 20.0nF to 44uF				
Dissipation Factor	NPO : Q≥ 1000 at 1KHz / X7R : 2.5%max. at 1KHz				
Insulation Resistance	10GΩ or 500/C Ω whichever is smaller				
Aging	NPO: 0%, X7R: 1.0% per decade of time typical				
Dielectric Withstanding Voltage	V ≤50V ; 200% Rated Voltage				
	100V ≤ V <500V ; 200% Rated Voltage				
	500V ≤ V <1KV ; 150% Rated Voltage				
	1000V = 120% Rated Voltage				
Tolerance	± 2% tolerances are only available in NPO				
Patent Number	M505047				

How To Order

Product Code	Stack and Size	Lead Configuration	Material	Capacitance (pF)	Tolerance	Rated Voltage	Packaging	Special test Requirement	Special Requirement
SMC: Commercial Size Switchmode Stacked Capacitor	stack Second Digit:	J: J Lead for h=0.070" L: L Lead for	Ex.: N: NP0 X: X7R B: X5R	Ex.: 103:10x10 ³ 224:22x10 ⁴ 475:47x10 ⁵	Ex.: G: +/-2.0% J: +/-5.0% K: +/- 10% M: +/- 20%	Ex.: 050: 50Vdc 101: 100Vdc 201: 200Vdc 501: 500Vdc 102:1000Vdc	Ex.: B: Bulk T:Tape&Reel W: Waffle pack	Ex.: Blank: Standard electrical test H: Hi-Rel Testing	Ex.; Blank: No special requirement 01~99: Customer specia requirement

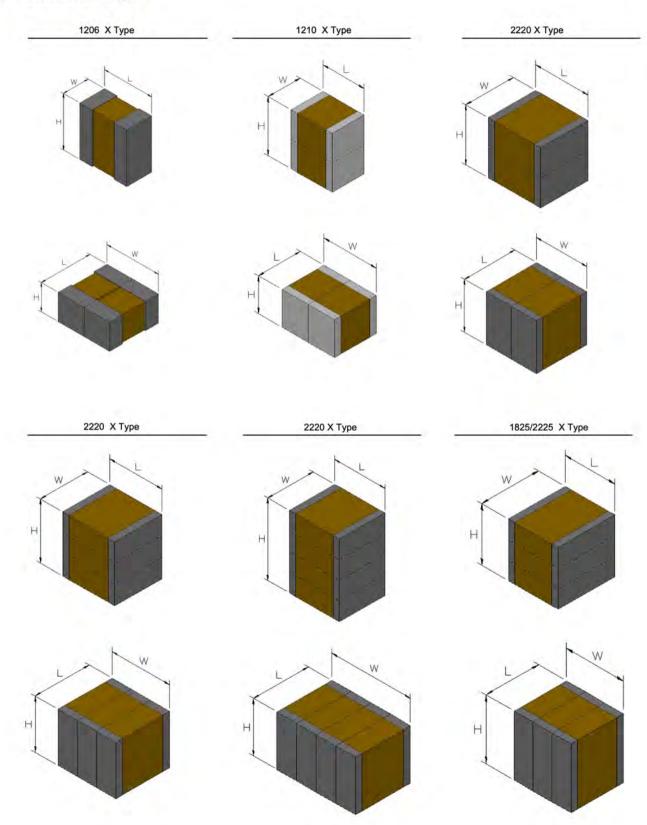


♦ Dimensional Shape





♦ Dimensional Shape



SMC Series - Stacked Capacitors



Dimensions

Unit: mm [inches]

EIA Size Code	12	10	18	112	22	20	18	25
Size Code	15	25	16	26	17	27	18	28
L	3.80 Max	3.80 Max	5.50 Max	5.50 Max	6.50 Max	6.50 Max	5.35±0.50	5.35±0.50
	[.150 Max]	[.150 Max]	[.217 Max]	[.217 Max]	[.256 Max]	[.256 Max]	[.210±.020]	[.210±.020]
W (max.)	2.90	2.90	4.00	4.00	5.50	5.50	6.85	6.85
	[.114]	[.114]	[.157]	[.157]	[.217]	[.217]	[.270]	[.270]
H(max.)	2.20	4.40	2.60	5.20	3.00	6.00	3.00	6.00
	[.087]	[.173]	[.102]	[.205]	[.118]	[.236]	[.118]	[.236]
S	1.00±0.10	1.00±0.10	1.65±0.50	1.65±0.50	1.65±0.50	1.65±0.50	1.65±0.50	1.65±0.50
	[.040±.004]	[.040±.004]	[.065±.020]	[.065±.020]	[.065±.020]	[.065±.020]	[.065±.020]	[.065±.020]
Р			2.54±0.25 [.100±.010]	2.54±0.25 [.100±.010]	2.54±0.25 [.100±.010]	2.54±0.25 [.100±.010]	2.54±0.25 [.100±.010]	2.54±0.25 [.100±.010]
h* (Typical)	1.30	1.30	1.30	1.30	1.30	1.30	1.78	1.78
	[.051]	[.051]	[.051]	[.051]	[.051]	[.051]	[.070]	[.070]
h* (P/S Type)							1.14 [.045]	1,14 [.045]
LL** (min.)					2.54 [.100]	2.54 [.100]	2.54 [.100]	2.54 [.100]
- t	2.25±0.1	2.25±0.1	3.08±0.1	3.08±0.1	4.50±0.10	4.50±0.10	0.60±0.10	0.60±0.10
	[.089±.004]	[.089±.004]	[.121±.004]	[.121±.004]	[.177±.004]	[.177±.004]	[.024±.004]	[.024±.004]
# of leads per side	3.6	1	1	1	j	1	3	3

EIA Size Code		1825		4		2225		
Size Code	38	48	58	19	29	39	49	59
L.	5.35±0.50	5.35±0.50	5.35±0.50	6.35±0.50	6.35±0.50	6.35±0.50	6.35±0.50	6.35±0.50
	[.210±.020]	[.210±.020]	[.210±.020]	[.250±.020]	[.250±.020]	[.250±,020]	[.250±.020]	[.250±.020]
W (max.)	6.85	6.85	6.85	6.85	6.85	6.85	6.85	6.85
	[.270]	[.270]	[.270]	[.270]	[.270]	[.270]	[.270]	[.270]
H(max.)	9.00	10.85	10.85	3.00	6.00	9.00	10.85	10.85
	[.354]	[.427]	[.427]	[.118]	[.236]	[.354]	[.427]	[.427]
S	1.65±0.50	1.65±0.50	1.65±0.50	1.65±0.50	1.65±0.50	1.65±0.50	1.65±0.50	1.65±0.50
	[.065±.020]	[.065±,020]	[.065±.020]	[.065±,020]	[.065±.020]	[.065±.020]	[.065±.020]	[.065±.020]
P	2.54±0.25	2.54±0.25	2.54±0.25	2.54±0.25	2.54±0.25	2,54±0.25	2.54±0.25	2.54±0.25
	[.100±.010]	[.100±.010]	[.100±.010]	[.100±.010]	[.100±.010]	[.100±.010]	[.100±.010]	[.100±.010]
h* (Typical)	1.78 [.070]	1.78 [.070]	1.78	1.78 [.070]	1.78 [.070]	1.78 [.070]	1.78 [.070]	1.78 [.070]
h* (P/S Type)	1.14	1.14 [.045]	1.14 [.045]	1.14	1.14 [.045]	1.14 [.045]	1.14 [.045]	1.14 [.045]
LL** (min.)	2.54	2.54	2.54	2.54	2.54	2.54	2.54	2.54
	[.100]	[.100]	[.100]	[.100]	[.100]	[.100]	[.100]	[.100]
t	0.60±0.10	0.60±0.10	0.60±0.10	0.60±0.10	0.60±0.10	0.60±0.10	0.60±0.10	0.60±0.10
	[.024±.004]	[.024±.004]	[.024±.004]	[.024±.004]	[.024±.004]	[.024±.004]	[.024±.004]	[.024±.004]
# of leads per side	3	3	3	3	3	3	3	3

^{• * &#}x27;h' varies depends on the lead style. See lead configuration above

 ^{** &}quot;LL" Applies only to Straight (N) leads



♦ X Type-Horizontal Dimensions

Unit: mm [inches]

EIA Size Code	1206	1210		1812			2220	, -
Size Code	22	25	26	36	46	27	37	47
L (max.)	3.60	3.50	4.90	4.90	4.90	6.20	6.10	6.10
	[.142]	[.138]	[.193]	[.193]	[.193]	[.244]	[.240]	[.240]
W (max.)	2.00	2.70	3.50	3.50	3.50	5.40	5.40	5.40
	[.079]	[.106]	[.138]	[.138]	[.138]	[.213]	[.213]	[.213]
H(max.)	3.90	5.40	5.40	6.80	9.00	6.20	8.00	10.60
	[.154]	[.213]	[.213]	[.268]	[.354]	[.244]	[.315]	[.417]

IA Size Code 1825					2225				
Size Code	28	38	48	58	29	39	49	59	
L (max.)	4.9 [.193]	4.9 [.193]	4.9 [.193]	4.9 [.193]	6.10 [.240]	6.10 [.240]	6.10 [.240]	6.10 [.240]	
W (max.)	6.75 [.266]	[.240] 6.75 [.266]							
H(max.)	5.40 [.213]	6.80 [.268]	9.00 [.354]	11.2 [.441]	6.20 [.244]	6.80 [.268]	9.00 [.354]	11.20 [.441]	

♦ X Type-Vertical Dimensions

Unit: mm [inches]

EIA Size Code	1206	1210		1812			2220	
Size Code	22	25	26	36	46	27	37	47
L (max.)	3.60	3.50	4.90	4.90	4.90	6.20	6.10	6.10
	[.142]	[.138]	[.193]	[.193]	[.193]	[.244]	[.240]	[.240]
W (max.)	3,90	5.40	5.40	6.80	9.00	6.20	8.00	10.60
	[.154]	[.081]	[.213]	[.268]	[.354]	[.244]	[.315]	[.417]
H(max.)	2.00	2.70	3.50	3.50	3.50	5.40	5.40	5,40
	[.079]	[.106]	[.138]	[.138]	[.138]	[.213]	[.213]	[.213]

IA Size Code 1825				2225				
Size Code	28	38	48	58	29	39	49	59
L (max.)	4.9	4.9	4.9	4.9	6.10	6.10	6.10	6.10
	[.193]	[.193]	[.193]	[.193]	[.240]	[.240]	[.240]	[.240]
W (max.)	5.40	6.80	9.00	11.20	6.20	6.80	9.00	11.20
	[.213]	[.268]	[.354]	[.441]	[.244]	[.268]	[.354]	[.441]
H(max.)	6.75	6.75	6.75	6.75	6.75	6.75	6.75	6.75
	[.266]	[.266]	[.266]	[.266]	[.266]	[.266]	[.266]	[.266]



Capacitance Range

EIA	Directords		NPO I	Maximum Capad	citance		X7R Maximum Capacitance				
Chip Size	Size Code	50V	100V	200V/250V	500V	1000V	50V	100V	200V/250V	500V	1000V
4000	12 (1×Cap)	104	104	223	472	332	475	335	564	683	223
1206	22 (2×Cap)	204	204	223	472	662	945	665	115	134	443
1210	15 (1×Cap)	104	104	473	123	153	106	475	564	124	473
1210	25 (2×Cap)	204	204	943	243	304	206	945	115	244	.943
1812	16 (1×Cap)	224	104	104	223	223	106	475	105	474	104
1612	26 (2×Cap)	444	204	204	443	443	206	945	205	944	204
2222	17 (1×Cap)	273	273	333	273	333	106	106	225	105	224
2220	27 (2×Cap)	543	543	663	543	663	206	206	445	205	444
	18 (1×Cap)	104	104	104	104	123	225	105	105	474	154
	28 (2×Cap)	204	204	204	204	243	445	205	205	944	304
1825	38 (3×Cap)	304	304	304	304	363	665	305	305	145	454
	48 (4×Cap)	404	404	404	404	483	885	405	405	185	604
	58 (5×Cap)	504	504	504	504	603	116	505	505	235	754
	19 (1×Cap)	823	823	333	153	153	475	475	225	474	104
	29 (2×Cap)	164	164	663	303	303	945	945	445	944	204
2225	39 (3×Cap)	244	244	993	453	453	146	146	665	145	304
	49 (4×Cap)	334	334	134	603	603	186	186	885	185	404
	59 (5×Cap)	414	414	164	753	753	236	236	116	235	504

[■] Other Stacked configuration on other sizes, capacitance values and voltages rating are available. Please contact Holy Stone.

Soldering and Handling Precautions

The recommended method for soldering large SMC capacitor, is reflow soldering. Wave soldering and manual soldering with Iron is not recommended.

Ceramic capacitors must be preheated with less than 2°C/sec rate to about 50°C below the reflow temperature. Sudden increase, or decrease in temperature more than the recommended rate, during soldering, may cause internal thermal cracks.



HVC Series Sp	ecifications & T	est Conditions
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Item	Specification	Test Conditions
Visual	No abnormal exterior appearance	Visual Inspection
Dimension	See HVC Series	Visual Inspection
Insulation Resistance	10,000M Ω or 500/C Ω , whichever is smaller	V≦500V, Rated Voltage V>500V, Applied 500Vdc Charge Time : 60 sec. Current : Less than 50mA
Flectrical	Capacitance Class I (NP0):	Class I (NP0):

Characterization

Within the specified tolerance.

Class II (X7R):

Within the specified tolerance.

Q/D.F.

Class I (NP0):

More Than 30pF : Q ≥ 1000 30pF & Below : Q ≥ 400 + 20C (C : Capacitance , pF)

Class II (X7R):

100V: 5.0% (C≧0.1uF) 100V: 2.5% (C<0.1uF) Other Voltage: 2.5% max

Capacitance	Frequency	Voltage
C≦ 1000pF	1MHz±10%	1.0±0.2Vrms
C>1000pF	1KHz±10%	1.0±0.2Vrms

Class II:

Frequ	uency	Voltage
X7R	1KHz±10%	1.0±0.2Vrms

Perform a heat temperature at 150±5°C for 30min, then place room temp. for 24±2 hours.

Withstanding Voltage

No dielectric breakdown or mechanical breakdown.

V < 500V: 200% Rated Voltage

500V≦V<1000V: 150% Rated Voltage

1000≦V: 120% Rated Voltage 2.5KV : 100% Rated Voltage

Voltage ramp up rate≦500V/sec for 1~5 sec. Charge/discharge current: less than 50mA

* Withstanding voltage testing requires immersion of the element in an isolation fluid prevent arcing on the chip surface, at voltage over 1000Vdc.

Temperature Capacitance Coefficient

Char.	Temp. Range	Cap. Change
NPO(N)	-55°C ~ +125°C	±30ppm/°C
X7R (X)	-55°C ~ +125°C	±15%

Class I:

[C2-C1/C1(T2-T1)] x 100%

Class II:

(C2-C1)/C1 x 100%

T1:Standard temperature(25°C)

T2:Test temperature

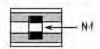
C1:Capacitance at standard temperature C2:Capacitance at test temperature

Adhesive Strength of Termination

No indication of peeling shall occur on the terminal electrode.

≦ 0603 size : 5N(≒ 0.5 Kg·f) > 0603 size : 10N(=1.0 Kg·f)

Pull force shall be applied for 10±1 sec.

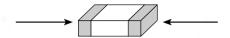




Item		Specificati	on	Test Conditions		
Resistance to Flexure of	Appearance	No mecha	nical damage shall occur.	The board should bend 1.0mm with a rate of 1.0 mm/sec.		
Substrate	Capacitance	Char.	Cap. Change	R230 Bending		
		NP0(N) X7R(X)	≤ ± 5.0% ≤ ± 12.5%	C Meter 45±1mm 45±1mm		

Solderability

More than 90% of the termination surface should be soldered so the metal part does not come out or dissolve.



Solder Temperature : 245±5°C Dip Time : 5 ± 0.5 sec.

Immersing Speed : 25±10% mm/s Solder : Lead Free Solder

Flux : Rosin

Preheat : At 80~120 °C For 10~30 sec.

Resistance to Soldering Heat

Appearance No mechanical damage shall occur.

Capacitance Class I (NP0):

Within \pm 2.5% or \pm 0.25pF, whichever

To satisfy the specified initial value.

is larger of initial value

Class II:

Char. Cap. change X7R(X) Within ± 10% Class II capacitor shall be set for 48± 4 hours at room temperature after one hour heat treatment at 150 +0/-10°C before initial measuring.

Preheat: at 150±10°C for 60~120sec. Dip: solder temperature of 260±5°C

Dip Time: 10 ± 1sec.

Immersing Speed: 25±10% mm/s

Flux : Rosin

Measure at room temperature after cooling for

Class II : 24 \pm 2 Hours Class II : 48 \pm 4 Hours

Temperature Cycle

Appearance No m

No mechanical damage shall occur

Capacitance

Q (Class I)

Insulation Resistance Withstand

Voltage

D.F. (Class II)

TIE AMERICANIES ABOUT 4 STAN STAN

Class I (NPO):

Within 2.5% or ±0.25pF, whichever

is larger of initial value

Class II:

Char. Cap. change X7R(X) Within ± 7.5%

Q (Class I) To satisfy the specified initial value.

D.F. (Class Π) To satisfy the specified initial value.

Insulation Resistance To satisfy the specified initial value.

Class II capacitor shall be set for 48 ± 4 hours at room temperature after one hour heat treatment at 150 +0/-10 °C before initial measuring.

Capacitor shall be subjected to five cycles of the temperature cycle as following:

Step	Temp.(°C)	Time(min)
1	Min. Rated Temp. +0/-3	30
2	25	3
3	Min. Rated Temp, +0/-3	30
4	25	3

Measure at room temperature after cooling for

Class I: 24 ± 2 Hours Class II: 48 ± 4 Hours



Item		Specification	Test Conditions	4.8	
Humidity	Appearance	No mechanical damage shall occur.	Class II capacitor shall be set for 48±4 hours room temperature after one hour heat treatment 150 +0/-10 °Cbefore initial measure.		
	Capacitance	Char. Cap. Change			
		NPO(N) Within ± 5.0% or ± 0.5pF, whichever is larger of initial value X7R(X) Within ± 15.0%	Temperature : 40± 2 °C Relative humidity : 90 ~95%RH Test Time : 500 +12/-0 hr		
	Q/D.F.	Class I (NP0) : More Than 30pF : Q ≧ 350 30pF & Below : Q ≧ 275+2.5xC (C : Capacitance , pF)	Measure at room temperature after cooling for Class I: 24 \pm 2 Hours Class II: 48 \pm 4 Hours		
		ClassⅡ(X7R): 100V: 10.0% max. (C≧0.1uF) 100V: 5.0% max. (C<0.1uF) Other Voltage: 5.0% max.			
	Insulation Resistance	1000M Ω or 50/C Ω , whichever Is Smaller. (C in Farad)			
High Temperature	Appearance	No mechanical damage shall occur.	voltage (following		
Load	Capacitance	Char. Cap. Change	table) is applied for one hour at maximum operation temperature ±3°C then shall be set for		
(Life Test) (100V-1KV)		NP0(N) Within ± 3.0% or ± 0.3pF, whichever is larger of	48±4 hours at room temperat measurement shall be conducted	ure and the initial	
		initial value	Applied Voltage :		
		X7R(X) Within ± 15%	Rated Voltage App	olied Voltage	
	Q/D.F.	Class I (NP0):		Rated Voltage	
	Q/D.I.	More Than 30pF : Q ≥ 350		Rated Voltage	
		30pF & Below : Q ≥ 275+2.5xC (C : Capacitance, pF)	250Vdc≦V≦500Vdc<0.1 uF 120%	Rated Voltage	
		(C. Capacitance, pr)	250Vdc≦V≦500Vdc≧0.1 uF 100%	Rated Voltage	
		Class II (X7R):	Less Than 1KVdc 120%	Rated Voltage	
		100V : 10.0% max. (C≧0.1uF)	More Than 1KVdc (include 1KV) 100%	Rated Voltage	
		100V: 5.0% max. (C<0.1uF)	As a service of the service of the		
	Insulation Resistance	Other Voltage : 5.0% max. 1000MΩ or 50/CΩ, whichever Is Smaller	Test Temperature: max. operating temp. Test Time: 1000 +12/-0 hours Current Applied: 50 mA Max.		
	resistance		Measure at room temperature at Class I: 24 ± 2 Hours Class II: 48 ± 4 Hours	ter cooling for	



Resistance

Item		Specification	Test Conditions
High Temperature Load (Life Test)	Appearance Capacitance	No mechanical damage shall occur. Char. Cap. Change	Class II capacitors applied DC voltage (following table) is applied for one hour at maximum operation temperature ±3°C then shall be set for 48±4 hours at
(1.5V-5KV)		NP0(N) Within ± 3.0% or ± 0.3pF, whichever is larger of initial value	room temperature and the initial measurement shall be conducted.
		X7R(X) Within ± 15%	Voltage Conditioning : 100% Rated Voltage meet MIL-PRF-
	Q/D.F.	Class I (NP0) : More Than 30pF : Q ≧ 350 30pF & Below : Q ≧ 275+2.5xC	49467(Group A/B) Current Applied: 50 mA Max. Measure at room temperature after cooling for
		(C : Capacitance , pF)	Class I: 24 ± 2 Hours Class II: 48 ± 4 Hours
		ClassⅡ(X7R) : 5.0% max.	Class II: 46 I 4 Hours
	Insulation Resistance	1000M Ω or 50/C Ω , whichever Is Smaller. (C in Farad)	
Vibration	Appearance	No mechanical damage shall occur	Solder the capacitor on P.C. Board shown in Fig 2.
			before testing.
	Capacitance	Class I (NP0):	before testing.
	Capacitance	Class I (NP0): Within 2.5% or ±0.25pF, whichever is larger of initial value	before testing. Vibrate the capacitor with amplitude of 1.5mm P-P changing the frequencies from 10Hz to 55Hz and
	Capacitance	Within 2.5% or ±0.25pF, whichever	Vibrate the capacitor with amplitude of 1.5mm P-P
	Capacitance	Within 2.5% or ±0.25pF, whichever is larger of initial value	Vibrate the capacitor with amplitude of 1.5mm P-P changing the frequencies from 10Hz to 55Hz and
	Capacitance Q (Class I)	Within 2.5% or ± 0.25 pF, whichever is larger of initial value Class Π : Char. Cap. Change	Vibrate the capacitor with amplitude of 1.5mm P-P changing the frequencies from 10Hz to 55Hz and back to 10Hz in about 1 min. Repeat this for 2 hours each in 3 perpendicular
	·	Within 2.5% or ±0.25pF, whichever is larger of initial value Class II: Char. Cap. Change X7R(X) Within ± 7.5%	Vibrate the capacitor with amplitude of 1.5mm P-P changing the frequencies from 10Hz to 55Hz and back to 10Hz in about 1 min. Repeat this for 2 hours each in 3 perpendicular



Resistance	See NCC : 10,000MΩ for rated vo	1 1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2	earance	Visual Inspection	on		
Insulation Resistance	10,000MΩ for rated vo	1 1 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2 2			Visual Inspection		
Resistance	for rated vo	or 500/C O w		Visual Inspection	on		
	10,000MΩ or 500/C Ω, whichever is smaller for rated voltage>10V and greater than 100/C Ω for rated voltage≤10V			Applied Voltage: Rated Voltage Charge Time: 60±5 sec. Charge-Discharge current shall be less the 50mA current.			
Electrical	Capacitance	e Class I (NP	0):	Class I (NP0):			
Characterization	47.4.4.4.4.4.4.4.4.4.4.4.4.4.4.4.4.4.4.		pecified tolerance.	Capacitance	Frequency	Voltage	
		ClassⅡ (X7	Class II (X7R/X7S/X6S/X5R): Within the specified tolerance.		1MHz±10%	1.0±0.2Vrms	
		Within the s			1KHz±10%	1.0±0.2Vrms	
	Q/D.F.	Class I (NP	0).	ClassII:	277-000		
	2,511.	More Than :	More Than 30pF : Q ≧ 1000 30pF & Below : Q ≧ 400+20C (C : Capacitance , pF)		Frequency	Voltage	
					1KHz±10%	*1.0±0.2Vrm or 0.5±0.2Vrms	
			R/X7S/X6S/X5R) : the value in Table 1.	C>10uF	120Hz±20%	0.5±0.2Vrms	
				Perform a heat temperature at 150±5°C for 36 minutes, then place room temp. for 24±2 hours. * Depend on the individual parts.			
Withstanding Voltage	No dielectric	breakdown or me	echanical breakdown.	250% of the rate discharge curren		~5 sec. charge / nA	
Temperature	Char.	Temp. Range	Cap. Change	Class I:			
		-55°C ~ +125°C	±30ppm/°C	[C2-C1/C1(T2-	-T1)] × 100%		
		-55°C ~ +125°C -55°C ~ +125°C	±15% ±22%	ClassII :			
		-55°C ~ +105°C	±22%	(C2-C1)/C1 x	100%		
		-55°C ~ +85°C	±15%	T1:Standard temperature (25°C) T2:Test temperature C1:Capacitance at standard temperature (25°C2:Capacitance at test temperature (T2) Under 1.0Vrms.			
	No indication electrode.	n of peeling shall	occur on the terminal	0201 : 2N (≒ 0 0402/0603 : 5N ≧0805- : 10N (±	(= 0.5 Kg·f)		
					be applied for 10	±1 sec.	



Item		Specification	on	Test Conditions		
Resistance to	Appearance	No mecha	nical damage shall occur.	The board should bend 1.0mm with a rate of 1.0 mm		
Flexure of Substrate	Capacitance	Char.	Cap. Change	per sec. The duration of the applied forces shall be 5 ± 1 sec		
778711711		NPO(N)	≤ ± 5.0%	■ ∠R230 ↓		
		X7R(X)	≤± 12.5%	Bending		
		X7S(R)	≤± 12.5%	Limit.		
		X6S(S)	≤ ± 12.5%	C Meter		
		X5R(B)	≤ ± 12.5%	45±1mm 45±1mm		

Solderability

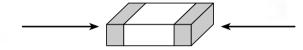
More than 90% of the termination surface should be soldered so the metal part does not come out or dissolve.

Solder Temperature : 245 \pm 5°C Dip Time : 5 \pm 0.5 sec.

Immersing Speed : 25±10% mm/s Solder : Lead Free Solder

Flux : Rosin

Preheat : At 80~120 °C For 10~30 sec.



Resistance to Soldering Heat

Appearance No mechanical damage shall occur.

Class II capacitor shall be set for 48± 4 hours at room temperature after one hour heat treatment at 150 +0/-10°C before initial measuring.

Capacitance Class I (NP0): Within \pm 2.5% or \pm 0.25pF, whichever is larger of initial value

Preheat : at 150±10°C for 60~120sec. Dip ; solder temperature of 260±5°C

Class II (X7R/X7S/X6S/X5R): ≤±7.5% of initial value Dip Time: 10 ± 1sec. Immersing Speed: 25±10% mm/s

(Class I) To satisfy the specified initial value.

Flux : Rosin

Q (Class I) To satisfy the specified initial value D.F, (Class II) Shell meet the value in Table 1.

Measure at room temperature after cooling for Class $I: 24 \pm 2$ Hours

Class II: 48 ± 4 Hours

Insulation Resistance

Capacitance

To satisfy the specified initial value.

Temperature Cycle

Appearance No mechanical damage shall occur

Class I (NP0):

Within ± 2.5% or ± 0.25pF, whichever is larger of initial value

Class II (X7R/X7S/X6S/X5R):

≤ ±7,5% of initial value

Q (Class I) To satisfy the specified initial value.

D.F. (Class II) Shell meet the value in Table 1.

Insulation To satisfy the specified initial value. Resistance

Class II capacitor shall be set for 48±4 hours at room temperature after one hour heat treatment at 150 +0/-10 °C before initial measuring.

Capacitor shall be subjected to five cycles of the temperature cycle as following:

Step	Temp.(°C)	Time(min)
1	Min. Rated Temp. +0/-3	30
2	25	3
3	Min. Rated Temp. +0/-3	30
4	25	3

Measure at room temperature after cooling for

Class I: 24 ± 2 Hours Class II: 48 ± 4 Hours



Item		Specification	Test Conditions
Humidity	Appearance Capacitance	No mechanical damage shall occur. Char. Cap. Change	Class II capacitor shall be set for 48±4 hours at room temperature after one hour heat treatment at 150 +0/-10 °Cbefore initial measure.
		NPO(N) Within ± 5.0% or ± 0.5pF, whichever is larger of initial value	Temperature : 40± 2 ℃ Relative humidity : 90 ~95%RH Test Time : Max. 500 hours
		$X7R(X)$ $\leq \pm 12.5\%$ $X7S(R)$ $\leq \pm 12.5\%$ $X6S(S)$ $\leq \pm 12.5\%$ $X5R(B)$ $\leq \pm 12.5\%$	Measure at room temperature after cooling for Class I: 24 \pm 2 Hours Class II: 48 \pm 4 Hours
	Q/D.F.	Class I (NP0): 30pF & Over: Q ≥350 10 to 30pF: Q ≥275+2.5C 30pF & Below: Q ≥200+10C Class II (X7R/X7S/X6S/X5R):	
	Insulation Resistance	Shell meet the value in Table 1. 1000MΩ or 50/C Ω, whichever is smaller for rated voltage>10V and greater than 10/C Ω for rated voltage≤10V (C in Farad)	

Humidity Load	Appearance	No mechanical damag		
	Capacitance	Char. Cap. Cha	voltage is applied for one hour operation temperature then shall be	
		NP0(N) Within ± 7.5% whichever is	or ± 0.75pF, hours at room temperature and	
		initial value	Applied Voltage :Rated Voltage	
		$X7R(X) \le \pm 12$	5% Temperature : 40± 2°C	
		X7S(R) ≤±12		
		X6S(S) ≤±12	5% Test Time: 500 Hrs Max.	
		X5R(B) ≤ ± 12	5% Current Applied : 50 mA Max.	
			Measure at room temperature after	er cooling for
	Q/D ₂ F,	Class I (NP0) : 30pF & Over : Q ≧350 10 to 30pF : Q ≧275+2 30pF & Below: Q ≧200	10C shall be set for 24±2 hours at room after one hour heat treatment at	temperature
		Class II (X7R/X7S/X6S	(5R): before final measure.	
		Shell meet the value in	able 1. Class II capacitor for Cap < 103(10nF	The state of the s
	Insulation Resistance	500M Ω or 25/C Ω , v smaller for rated voltagreater	4 HOURS	ooung for 46 ±
		5/C Ω for rated voltag	≤10V (C in	



Item		Specification	Test Conditions
High Temperature Load	Appearance Capacitance	No mechanical damage shall occur Char. Cap. Change	for one hour at maximum operation temperature
(Life Test)		NPO(N) Within ± 5.0% or ± 0.5pF, whichever is larger of	then shell be set for 48± 4 hours at room temperature and the initial measurement shall be conducted.
		initial value $X7R(X) \le \pm 12.5\%$ $X7S(R) \le \pm 12.5\%$ $X6S(S) \le \pm 12.5\%$ $X5R(B) \le \pm 12.5\%$	Applied Voltage: Rated Voltage The class I applied voltage 200% of rated voltage. Temperature: max. operation temperature Test Time: Max 1000 hours.
	Q/D.F.	Class I (NP0): 30pF & Over: Q ≥350 10 to 30pF: Q ≥275+2.5C 30pF & Below: Q ≥200+10C Class II (X7R/X7S/X6S/X5R): Shell meet the value in Table 1.	Current Applied : 50mA Max Measure at room temperature after cooling for Class I : 24 ± 2 Hours Class II capacitor for Cap≧103(10nF) shall be set for 24±2 hours at room temperature after one hour heat treatment at 150 +0/-10°C before final measure.
	Insulation Resistance	1000MΩ or 50/C Ω, whichever is smaller for rated voltage>10V and greater than 10/C Ω for rated voltage≤10V (C in Farad)	Class II capacitor for Cap $<$ 103(10nF) Measure at room temperature after cooling for 48 \pm 4 Hours.

Vibration	Appearance	No mechanical damage shall occur.	Solder the capacitor on P.C. board.
	Capacitance	Within the specified tolerance.	Vibrate the capacitor with amplitude of 1.5mm P-P changing the frequencies from 10Hz to 55Hz and
	Q (Class I)	To satisfy the specified initial value.	back to 10Hz in about 1 min.
	D.F. (Class II)	Shell meet the value in Table 1.	Repeat this for 2 hours each in 3 perpendicular directions



Table 1

Temp char: X7R, X7S, X6S, X5R

DC 4V				D.F.	(MAX)
DC 6.3V DC 16V DC 16V DC 16V DC 25V DC 25V DC 25V DC 50V DC 6.3V DC 20 %	Capacitance Range		Capacitance Range	Vibration Resistance to solder heat	Humidity Humidity loading High temperature loading
DC 6.3V DC 16V DC 16V DC 16V DC 25V DC 25V DC 25V DC 50V DC 6.3V DC 20 %	Т	4V		10.0%	20.0%
DC 16V DC 25V				10.0%	
DC 16V DC 25V		10V	10 a 10 70 a 10 70	10.0%	20.0%
DC 25V DC 50V			All Capacitance		20.0%
DC 50V			100		20.0%
DC 4V					
DC 6.3V C<22uF 10.0% 20.0% 30.0% 30.0% 20.0% 30.0% 20.0% 30.0% 20.0%			All Capacitance		
0402 DC 10V					
DC 10V		6.3V			
DC 16V DC 25V All Capacitance 10.0% 20.0%		10V			
DC 25V All Capacitance 10.0% 20.0% DC 35V 10.0% 20.0% DC 6.3V 10.0% 20.0% DC 10V 10.0% 20.0% DC 16V 10.0% 20.0% DC 25V 10.0% 20.0% DC 35V 10.0% 20.0% DC 35V 10.0% 20.0% DC 50V 10.0% 20.0% DC 4V 10.0% 20.0% DC 16V 10.0% 20.0% DC 16V 10.0% 20.0% DC 16V 10.0% 20.0% DC 35V 10.0% 20.0% DC 10V 10.0% 20.0% DC 35V 10.0% 20.0% DC 4V C=22uF 15.0% 25.0% DC 4V C≤10uF 10.0% 20.0% DC 6.3V C≤10uF 10.0% 20.0% DC 10V C≤10uF 10.0% 20.0% DC 10V C≤10uF 10.0% 20.0% DC 35V DC 35V 10.0% 20.0% DC 16V DC 35V All Capacitance 10.0% 20.0% DC 10V C≤10uF 10.0% 20.0% DC 10V C≤10uF 10.0% 20.0% DC 35V DC 35V All Capacitance 10.0% 20.0% DC 35V DC 35V 10.0% 20.0% DC 35V DC 35V 10.0% 20.0% DC 4V DC 50V 10.0% 20.0% DC 4V DC 35V 10.0% 20.0% DC 4V DC 6.3V 10.0% 20.0% DC 4DC 5DC 5DC 5DC 5DC 5DC 5DC 5DC 5DC 5DC 5					
DC 35V DC 50V 10.0% 20.0%			All Capacitance		
DC 50V			Lift askabitation		
DC 6.3V					
DC 10V DC 16V DC 16V DC 25V DC 35V DC 50V DC 35V DC 10.0% DC 20.0%					
DC 16V DC 25V DC 35V DC 35V DC 50V DC 6.3V DC 6.3V DC 50V DC 50V DC 6.3V DC 10.0% DC 0.0%					
DC 25V			Law Laboratory		
DC 35V DC 50V 10.0% 20.0%			All Capacitance		
DC 50V					
DC 4V					
DC 6.3V DC 10V DC 10V DC 16V DC 16V DC 25V DC 35V DC 4V C≤100F DC 10V C≤100uF DC 10V C≤100uF DC 35V DC 35V DC 35V DC 35V DC 35V DC 6.3V DC 50V DC 50V	-				
DC 10V DC 16V DC 25V DC 35V DC 50V DC 4V C≤2uF 10.0% 20.0%					
DC 16V	All Capacitance				
DC 25V DC 35V 10.0% 20.0%					
DC 35V DC 50V 10.0% 20.0%					
DC 50V					
DC 4V			A		
DC 6.3V			C=22uF		
DC 6.3V C≦100uF 15.0% 25.0% C≤10uF 10.0% 20.0% C≤100uF 15.0% 25.0% DC 16V 10.0% 20.0% DC 25V All Capacitance 10.0% 20.0% DC 35V DC 50V 10.0% 20.0% DC 4V DC 6.3V 10.0% 20.0% DC 6.3V 10.0% 20.0% DC 6.3V 20.0%		- III			
1206 DC 10V C≤100F 15.0% 25.0% DC 16V DC 25V DC 35V DC 50V DC 50V DC 4V DC 6.3V C≤100uF 10.0% 10.0% 20.0% 10.0% 20.0% 10.0% 20.0% 20.0% 10.0% 20.0% 20.0% 20.0% 20.0% 20.0% 20.0% 20.0% 20.0% 20.0%		6.3V			
1206 DC 16V C≦100uF 15.0% 25.0% DC 16V 10.0% 20.0% DC 25V All Capacitance 10.0% 20.0% DC 35V 10.0% 20.0% DC 50V 10.0% 20.0% DC 4V 10.0% 20.0% DC 6.3V 10.0% 20.0% DC 6.3V 20.0% DC 0.0% 20.0%					
DC 16V 10.0% 20.0% DC 25V 10.0% 20.0% DC 35V 10.0% 20.0% DC 50V 10.0% 20.0% DC 4V 10.0% 20.0% DC 6.3V 10.0% 20.0%		10V			
DC 25V All Capacitance 10.0% 20.0% DC 35V 10.0% 20.0% DC 50V 10.0% 20.0% DC 4V 10.0% 20.0% DC 6.3V 10.0% 20.0%		161/	0=100ui		
DC 35V All Capacitance 10.0% 20.0% DC 50V 10.0% 20.0% DC 4V 10.0% 20.0% DC 6.3V 10.0% 20.0%			Local Test		
DC 50V 10.0% 20.0% DC 4V 10.0% 20.0% DC 6.3V 10.0% 20.0%		35V	All Capacitance		
DC 4V 10.0% 20.0% DC 6.3V 10.0% 20.0%			1 2 1 2 2 2 2 3		
DC 6.3V 10.0% 20.0%	Ė				
			100		
1 100 000 1 20.0%				10.0%	20.0%
1210 DC 16V All Capacitance 10.0% 20.0%			All Capacitance		
DC 25V 10.0% 20.0%			- sir oupauliance		
DC 35V 10.0% 20.0%			1		
DC 50V 10.0% 20.0%			h		
DC 6.3V 10.0% 20.0%	+				1
DC 10V 10.0% 20.0%			1		
DC 16V 10.0% 20.0%			Victoria de la Constantina del Constantina de la		
1812 DC 25V All Capacitance 10.0% 20.0%			All Capacitance		
DC 35V 10.0% 20.0%					
DC 50V 10.0% 20.0%					
2220 DC 63V All Capacitance 10.0% 20.0%			All Canacitance		



Item		Specification	AEC	-Q200 Test Cond	itions
Pre- and Post- Stress Electrical Test	8	-		2	
High Temperature Exposure	Appearance Capacitance	No mechanical damage shall occur. Char. Cap. Change		erature : max. rated : 1000+12/-0 hours	I temperature.
(Storage)	Q/D.F.	NP0(N) Within 2.5% or ±0.25pF, whichever is larger of initial value X7R(X) ≤ ±10% of initial value Class I (NP0):	Measure a 24±2 hours	t room temperature s.	after cooling for
	Q/D.F.	To satisfy the specified initial value Class II (X7R):			
		Shell meet the value in Table 1.			
	Insulation Resistance	To satisfy the specified initial value			
Temperature	Appearance	No mechanical damage shall occur.		pacitor shall be se	
Cycle	Capacitance	Char. Cap. Change		at 150 +0/-10 °	
		NPO(N) Within 2.5% or ±0.25pF, whichever is larger of initial value X7R(X) ≤ ±10% of initial value	Capacitor shall be subjected to 1000 cycles the temperature cycle as following:		
	Q/D.F.	Class I (NP0):	Step	Temp.(°C)	Time(min)
		To satisfy the specified initial value	1	-55+0/-3	15±3
		Class II (X7R):	2	Room Temp.	1
		Shell meet the value in Table 1:	3	125+3/-0	15±3
	Insulation Resistance	To satisfy the specified initial value	4	Room Temp.	. thi
	Resistance		Measure a 24±2 hour	t room temperature s.	after cooling for
			Solder the Fig 2. befo	capacitor on P.C. re testing.	board shown in
Destructive Physical Analysis		No defects or abnormalities.	Per EIA-46	59	
Biased	Appearance	No mechanical damage shall occur.	Class II capacitors applied DC voltage of the rated voltage is applied for one hour a maximum operation temperature ± 3°C, the		
Humidity	Capacitance	Char. Cap. Change			
		NP0(N) Within ± 7.5% or ±0.75pF, whichever is larger of initial value	whichever is larger of be conducted		
		$X7R(X) \le \pm 12.5\%$ of initial value	Applied \	/oltage : Rated	voltage(500Vdd
	Q/D.F.	Class I (NP0) : More Than $30pF : Q \ge 350$ $30pF \& Below : Q \ge 275 + 2.5xC$ (C : Capacitance , pF)	Applied Voltage: Rated voltage(500Vdc max.) and DC 1.3 to 1.5V. Add 100Kohn resistor. Temperature: 85±3°C Relative Humidity: 80 to 85 %RH Test Time: 1000 ±12/-0 hours		

Class II (X7R):

Smaller

Insulation

Resistance

Shell meet the value in Table 1.

 $500M\Omega$ or $25/C\Omega$, whichever Is

Test Time: 1000 +12/-0 hours

Current Applied: 50 mA Max.

24±2 hours.

Measure at room temperature after cooling for



Item		Spe	cification	AEC-Q200 Test Conditions		
Operational Life	Appearance Capacitance	No mech	anical damage shall occur. Cap. Change	Class ☐ capacitors applied DC voltage (follow		
		NPO(N)	Within ± 5% or ±0.5pF, whichever is larger of initial value ≤ ±15% of initial value	table) is applied for or operation temperature ±3s 24±2 hours at room temperature temperature temperaturement shall be confided. Applied Voltage:	C then shall be set erature and the init	
	Q/D.F.	Class I (NP0):			
	45,671		an 30pF : Q ≧ 350	Rated Voltage	Applied Voltage	
			Below:Q ≥ 275 + 2.5×C	V<100Vdc, Class I	200%Rated Voltage	
		(C: Cap	acitance , pF)	V<100Vdc, ClassⅡ	100%Rated Voltage	
		Class II (X7R):	V<250Vdc<1.0uF	150%Rated Voltage	
			et the value in Table 1.	V≦250Vdc≧1.0 uF	100%Rated Voltage	
	Insulation	1000MO	or 50/CΩ, whichever Is	250Vdc≦V≦500Vdc<0.1 uF	120%Rated Voltage	
	Resistance	Smaller	50 S 10 S 10 A 10 S 10 S 10 S 10 S 10 S 1	250Vdc≦V≦500Vdc≧0.1 uF	100%Rated Voltage	
				Less Than 1KVdc	120%Rated Voltage	
				More Than 1KVdc (include 1KV)	100%Rated Voltage	
				(C : Capacitance) Test Temperature : 125±35 Test Time : 1000 +12/-0 he Current Applied : 50 mA M Measure at room tempera 24±2 hours.	ours ax.	
External Visual		No defec	ts or abnormalities.	Visual Inspection		
Physical Dimension		Within Th	ne Specified Dimensions	Using Calipers		
Resistance to Solvents	Appearance Capacitance		ng defects. ne Specified Tolerance	It is applicable to marked and/or coated components.		
	Q / D.F. Class I (NP0): To satisfy the specified i		NP0): y the specified initial value.	Per MIL-STD-202 Method Solvent 1:		
		Lo satisfy the specified initial value		1 part (by volume) of isopropyl alcohol 3 parts (by volume) of mineral spirits, or three parts (by volume) of a mixture of 80% (by volume) of kerosene and 20% (volume) ethylbenzene. Solvent 4: 42 parts (by volume) of water 1 part (by volume) of propylene glycol monomethyl ether 1 part (by volume) of monoethanolamine		
	Insulation Resistance					
Mechanical Shock	Appearance Capacitance		anical damage shall occur. le Specified Tolerance	Solder the capacitor on P. Fig 2. before testing.	C. Board shown in	
	Q / D.F.	Class I (I	NP0) : / the specified initial value.	Three shocks in each applied along 3 mutually	perpendicular axes	
		ClassII (2 Shell me	X7R) : et the value in Table 1.	of the test specimen (18 shocks). The specified test pulse should be Half-sin		
	Insulation Resistance To satisfy the specified initial value			and should have a duration: 0.5ms, pea value: 1500g and velocity change: 15.4 ft/sec		



ACC Series S	pecifications 8	Test Conditions
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Item		Specification	AEC-Q200 Test Conditions	
Vibration	Appearance Capacitance	No mechanical damage shall occur. Within The Specified Tolerance	Solder the capacitor on P.C. Board shown in Fig 2. before testing.	
	Q / D.F. Class I (NP0): To satisfy the specified initial value.		Perform 12 cycles in each of the 3 mutually perpendicular axes of the capacitor (in total	
	440700000	Class II (X7R): Shell meet the value in Table 1.	36 cycles). Subject the capacitor to a simple harmonic motion varying the frequency logarithmically between 10 and 2000 Hz and	
	Insulation Resistance	To satisfy the specified initial value	return to 10 Hz (duration approx. 20 min) with an amplitude of 1.5 mm	
Resistance to Soldering	Appearance Capacitance	No mechanical damage shall occur. Char. Cap. Change	Class II capacitor shall be set for 24±2 hours at room temperature after one hour heat	
Heat		NP0(N) Within 2.5% or ±0.25pF, whichever is larger of	treatment at 150 +0/-10°C before initial measure.	
		Initial value X7R(X) ≤ ±10% of initial value	Preheat : At 150± 10°C For 60-120sec	
	Q/D.F.	Class I (NP0) : To satisfy the specified initial value.	Dip : Solder Temperature of 260± 5°C Dip Time : 10 ± 1 sec.	
		ClassII (X7R) : Shell meet the value in Table 1.	Measure at room temperature after cooling for 24±2 hours.	
	Insulation To satisfy the specified initial value			
ESD	Appearance Capacitance	No mechanical damage shall occur. Within The Specified Tolerance	Per AEC-Q200-002	
	Q / D.F.	Class I (NP0): To satisfy the specified initial value.		
		Class II (X7R) : Shell meet the value in Table 1.		
	Insulation Resistance	To satisfy the specified initial value		
Solderability	100000000000000000000000000000000000000	of the termination surface should the metal part does not come out	(a)Preheat at 155°C for 4 hours, immerse the capacitor in flux. Immerse in solder bath for 5+0/-0.5 sec. at 245±5°C	
	→	←	(b)Should be placed into steam aging for 8 hours 15 minutes. After preheating, immerse the capacitor in flux. Immerse in solder bath for 5+0/-0.5 sec. at 245±5°C	
			(c) Should be placed into steam aging for 8 hours 15 minutes. After preheating, immerse the capacitor in flux. Immerse in solder bath for 120±5 sec. at 260±5°C	
Board Flex	Appearance Capacitance	No mechanical damage shall occur. Char. Cap. Change	Bending shall be applied to the 3.0 mm for X and 3mm for NP0 with rate of 1.0mm/sec.	
		$\frac{NPO(N)}{X7R(X)}$ ≤ ±5% of initial value ≤ ±12.5% of initial value	The duration of the applied forces shall be 60 se	
			R230 Bending	



Item		Specification	AEC-Q200 Test Conditions			
Electrical	Appearance	No mechanical damage shall occur.	Visual Inspection			
Characterization	Capacitance	Within The Specified Tolerance	Class I (NP0):			
	Q/D.F.	Class I (NP0):	Capacitance	Frequency	Voltage	
		More Than 30pF : Q ≥ 1000	C<1000pF	1MHz±10%	1.0±0.2Vrms	
		30pF & Below : Q ≥ 400+20C (C : Capacitance , pF)	C≧1000pF	1KHz±10%	1.0±0.2Vrms	
			ClassⅡ(X7R)			
		ClassII(X7R): Shell meet the value in Table 1.	Frequ	ency	Voltage	
			X7R	1KHz±10%	1.0±0.2Vrms	
			Perform a heat temperature at 150±3°C for 30min, then place room temp. for 24±2 hours.			
	Insulation Resistance	1000M Ω or 50/C Ω , whichever Is Smaller	V≦500V, Rated Voltage V>500V, Applied 500Vdc Charge Time : 120sec. Is applied less than 50mA current.		rrent.	
	Withstanding Voltage	No dielectric breakdown or mechanical breakdown	V<100V : 250% Rated Voltage 100V≦V<500V: 200% Rated Voltage 500V≦V<1000V: 150% Rated Voltage 1000V≦V: 120% Rated Voltage			
			for 1-5 sec. Current is limited to less than 50mA.			
Terminal Strength	Appearance Capacitance	No mechanical damage shall occur. Within The Specified Tolerance	Apply a 18N force in parallel with the test ji for 60 sec.			
	Q / D.F.	Class I (NP0): To satisfy the specified initial value.	Chip Size 060 with the test jig		N force in parallel	
	Class II (X7R) : Shell meet the value in Table 1.			10N / 18N		
	Insulation Resistance	To satisfy the specified initial value		TOIN / TOIN		
Poom Load Tost	The ohin and in	on following force:	Place the can	scitor in the	neam load fivture	

Beam Load Test

The chip endures following force:

Chip Length	Chip Thickness	Force Min.	
≦2.5mm	>0.5mm	20N	
566	<1.25mm	15N	
≧3.2mm	≧1.25mm	54.5N	

Place the capacitor in the beam load fixture. Apply a force.



Speed supplied the Stress Load: 2.5mm / sec.



Table 1

Temp char: X7R

		1	D.F.(MAX)		
Size	Rated Voltage	Capacitance Range	High Temperature Exposure Temperature cycle Resistance to Solvents Mechanical Shock Vibration Resistance to solder heat ESD Initial Terminal Strength	Biased Humidity Operational Life	
	DC 16V	All Capacitance	10.0%	20.0%	
0603	DC 25V	All Capacitance	10.0%	20.0%	
U. Cristi	DC 50V	All Capacitance	10.0%	20.0%	
	DC 16V	All Capacitance	10.0%	20.0%	
	DC 25V	All Capacitance	10.0%	20.0%	
0005	DC 50V	All Capacitance	10.0%	20.0%	
0805	DO 400V	C≧0.1uF	5.0%	10.0%	
	DC 100V	C<0.1uF	2.5%	5.0%	
	>DC 100V	All Capacitance	2.5%	5.0%	
	DC 25V	All Capacitance	10.0%	20.0%	
	DC 50V	All Capacitance	10.0%	20.0%	
1206	DC 100V	C≧0.1uF	5.0%	10.0%	
	DC 100V	C<0.1uF	2.5%	5.0%	
	>DC 100V	All Capacitance	2.5%	5.0%	
	DC 50V	All Capacitance	10.0%	20.0%	
1210	DC 100V	C≧0.1uF	5.0%	10.0%	
1210	DC 100V	C<0.1uF	2.5%	5.0%	
- 11	>DC 100V	All Capacitance	2.5%	5.0%	
	DC 50V	All Capacitance	10.0%	20.0%	
1812	DC 100V	C≧0.1uF	5.0%	10.0%	
1012	DC 100V	C<0.1uF	2.5%	5.0%	
	>DC 100V	All Capacitance	2.5%	5.0%	
	DC 50V	All Capacitance	10.0%	20.0%	
2220	DC 100V	C≧0.1uF	5.0%	10.0%	
2220	DC 100V	C<0.1uF	2.5%	5.0%	
	>DC 100V	All Capacitance	2.5%	5.0%	

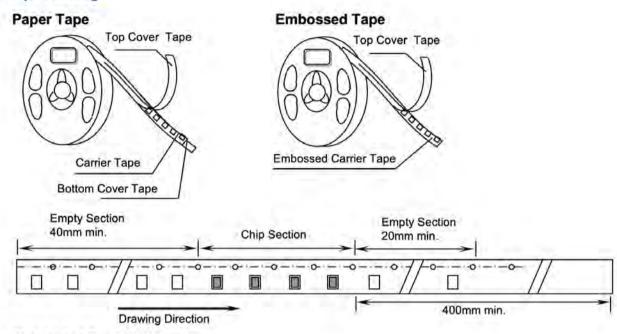


Multilayer Ceramic Chip Capacitor

Bulk Packing

Standard packing 10Kpcs/pack, others according to customers' request.

Tape Packing



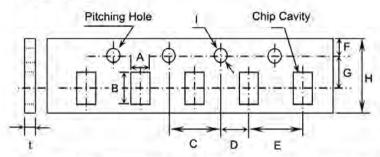
Material And Quantity (¢180mm)

Chip Size		Dimens	sion (mm)	¢180mm reel			
(EIA Code)	L	W	Ţ	Paper Tape	Plastic Tape		
0201	0.6	0.3	T≤0.39	15,000 pcs/reel	N/A		
0402	1	0.5	T≤0.70	10,000 pcs/reel	N/A		
0603	1.6	0.8	T≤1.00	4,000 pcs/reel	N/A		
0005	2	4.0F	T≤1.00	4,000 pcs/reel	N/A		
0805	2	1.25	T>1.00	N/A	3,000 pcs/reel		
			T≤1.00	4,000 pcs/reel	N/A		
1206	3.2	1.6	1.00 <t≤1.25< td=""><td>N/A</td><td>3,000 pcs/reel</td></t≤1.25<>	N/A	3,000 pcs/reel		
		100	T>1.25	N/A	2,000 pcs/reel		
			T≤1.25	N/A	3,000 pcs/reel , 2,000 pcs/reel		
1210	3.2	2.5	1.25 <t≤2.40< td=""><td>N/A</td><td>2,000 pcs/reel , 1,000 pcs/reel</td></t≤2.40<>	N/A	2,000 pcs/reel , 1,000 pcs/reel		
			T>2.40	N/A	500 pcs/reel , 1,000 pcs/reel		
	-		T≤1.25	N/A	3,000 pcs/reel , 2,000 pcs/reel		
1808	4.6	2	1.25 <t≤2.40< td=""><td>N/A</td><td>2,000 pcs/reel , 1,000 pcs/reel</td></t≤2.40<>	N/A	2,000 pcs/reel , 1,000 pcs/reel		
			T>2.40	N/A	500 pcs/reel , 1,000 pcs/reel		
4042	40	2.2	T≤2.20	N/A	1,000 pcs/reel		
1812	4.6	3.2	T>2.20	N/A	700 pcs/reel		
1005	16	6.25	T≤2.20	N/A	700 pcs/reel		
1825	4.6	6.35	T>2.20	N/A	400 pcs/reel		
2208	5.7	2	T≤2.20	N/A	1,000 pcs/reel		
2211	E 7	20	T≤2.20	N/A	1,000 pcs/reel		
2211	5.7	2.8	T>2.20	N/A	700 pcs/reel		
2220	5.7	5	T≤2.20	N/A	1,000 pcs/reel		
2220	5.7	9	T>2.20	N/A	700 pcs/reel		
2225	E 7	6.25	T≤2.20	N/A	700 pcs/reel		
2225	5.7	6.35	T>2.20	N/A	400 pcs/reel		



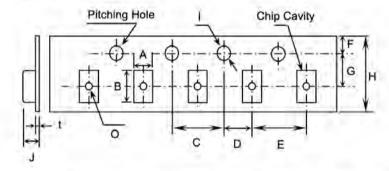
Tape Dimensions and Specifications

Paper Tape



			0 0	8		Unit : mm
	0201	0402	0603	0805	1206	1210
A	0.37±0.1	0.61±0.1	1.10±0.2	1.50±0.2	1.90±0.2	2.90±0.2
В	0.67±0.1	1.20±0.1	1.90±0.2	2.30±0.2	3.50±0.2	3.60±0.2
CD	4.00±0.1	>				
D	2.0±0.05	>	N	7/-		
E	2.00±0.1	>	4.00±0.1	>		
F	1.75±0.1	>		7.7		
G	3.5±0.05	>				
H	8.00±0.3	>				
$\Gamma =$	¢1.5+0.1/-0	>				_
t	1.1 max.	>		-		

Embossed Tape

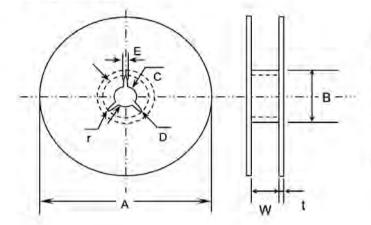


1.6	les is	 in	-
U	m	 m	ш

_	0805	1206	1210	1808	2208	1812	1825	2211	2220	2225
A	1.5±0.2	1.9±0.2	2.9±0.2	2.5±0.2	2.5±0.2	3.6±0.2	6.9±0.2	3.2±0.2	5.4±0.2	6.9±0.2
В	2.3±0.2	3.5±0.2	3.6±0.2	4.9±0.2	6.1±0.2	4.9±0.2	4.9±0.2	6.1±0.2	6.1±0.2	6.1±0.2
C	4.0±0.1	-								
D	2.0±0.05.	-					-			
E	4.0±0.1	-				8.0±0.1	-			
F	1.75±0.1	→								
G	3.5±0.05.	-		5.5±0.05	-					
H	8.0±0.3	->		12.0±0.3	•					
1	¢1.5+0.1/-0	→						-		
J	3.0 max.	->		4.0 max.	-					
1	0.3 max.	-								
0	1.0±0.1	-		1.5±0.1	→					

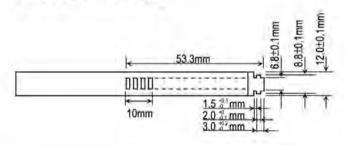


Reel Dimensions

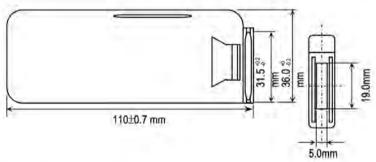


0402 to 1210	
0402 10 12 10	1808 to 2220
¢ 382 max.	¢ 178±2.0
¢ 50 min.	¢ 60±0.2
¢ 13±0.5	¢ 13±0.5
¢ 21±0.8	¢ 21±0.8
¢ 2.0±0.5	¢ 2.0±0.5
10±0.15	13±0.3
2.0±0.5	17±1.4
1.0	1.0
	¢ 382 max. ¢ 50 min. ¢ 13±0.5 ¢ 21±0.8 ¢ 2.0±0.5 10±0.15 2.0±0.5

Bulk Cassette Packing



Cassette Pa	acking	Unit : mm	
Chip Size	0402	0603	2012
Length	1.00±0.05	1.60±0.10	2.00±0.20
Width	0.50±0.05	0.80±0.10	1.25±0.20
Thickness	0.50±0.05	0,80±0.10	0.60±0.10
Quantity	50,000pcs	15,000pcs	10,000pcs



Cover Tape Peel Force



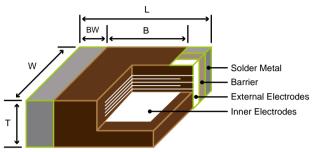
The peel force of cover tape is 5 to 70 grams in the direction of arrow.



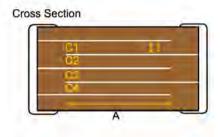
The Multilayer Ceramic Chip Capacitors supplied in bulk, cassette or taped & reel package are ideally suitable for thick-film Hybrid circuits and automatic surface mounting on printed circuit boards.

Mainly used in electric circuit for by-pass, filtering and smoothing circuit.

Shapes and Dimensions



	BW B	→
T T		Solder Metal Barrier External Electrodes Inner Electrodes
		Dimension (mm)



C	AN
$C = \varepsilon^0 \cdot \varepsilon$	t

C: Capacitance

ε₀: Dielectric constant in the air ε : Proportional dielectric constant

A: Overlap Area

t : Dielectric Thickness

N: Layers

				Dimension	on (mm) [inches]
EIA style	i.	W	Tmax.	BWmin	Bmin.
0201	0.60±0.03	0.30±0.03	0.33	0.10	0.20
	[.024±.002]	[.011±.002]	[.013]	[.004]	[.008]
0402	1.00±0.05 [.039±.002]	0.50±0.05 [.020±.002]	0.55 [.022]	0.15 [.006]	0.30 [.012]
0603	1.60±0.10	0.80±0.10	1.00	0.15	0.40
	[.063±.004]	[.031±.004]	[.039]	[.006]	[.016]
0805	2.00±0.20	1.25±0.20	1.45	0.20	0.70
	[.079±.008]	[.049±.008]	[.057]	[.008]	[.028]
1206	3.20±0.30	1.60±0.20	1.80	0.30	1.50
	[.126±.012]	[.063±.008]	[.071]	[.012]	[.059]
1210	3.20±0.30 [.126±.012]	2.50±0.20 [.098±.008]	2.60 [.102]	0.30	1.60 [.063]
1808	4.60±0.30	2.00±0.20	2.20	0.30	2.50
	[.181±.012]	[.079±.008]	[.087]	[.012]	[.098]
1812	4.60±0.30 [.181±.012]	3.20±0.30 [.126±.012]	3.00 [.118]	0.30 [.012]	2.50 [.098]
1825	4.60±0.30 [.181±.012]	6.35±0.40 [.250±.016]	3.40 [.118]	0.30 [.012]	2.50 [.098]
2208	5.70±0.40	2.00±0.20	2.20	0.30	3.50
	[.220±.016]	[.197±.008]	[.087]	[.012]	[.137]
2211	5.70±0.40	2.80±0.40	3.00	0.30	3.50
	[.220±.016]	[.110±.016]	[.118]	[.012]	[.137]
2220	5.70±0.40	5.00±0.40	3,40	0.30	3,50
	[.220±.016]	[.197±.016]	[.133]	[.012]	[,137]
2225	5.70±0.40	6.35±0.40	3.40	0.30	3.50
	[.220±.016]	[.250±.016]	[.133]	[.012]	[.137]

Nominal Capacitance and Tolerance

1. Standard Combination of Nominal Capacitance and Tolerance

Cla	ss EIA Symbol	the telephone visitings sale	
ī	NP0	J (±5%),K (±10%)	E-12
П	X7R	K(±10%), M(±20%)	E-3,E-6 E-12 Series
	X7T	K(±10%), M(±20%)	E-3,E-6 Series
	X7P	K(±10%), M(±20%)	E-3,E-6 Series
	X6S	K(±10%), M(±20%)	E-3,E-6 Series
	X5R	K(±10%), M(±20%)	E-3,E-6 Series

2. E Se	eries (Sta	ndard Nu	imber)			
E- Series		App	olication (Capacitar	nce	
E-3		.0	2	.2	4.	7
E-6	1.0	1.5	2.2	3.3	4.7	6.8
E12	1.0 1.2	<u>1.5</u> <u>1.8</u>	2.2 2.7	3.3 3.9	4.7 5.6	6.8 8.2



♦EIA Designations

For Class I Dielectrics

Coefficie capacita (ppm/ °C	nce	Multiplier a to column	pplicable	Tolerance of temp, coeff.(ppm/ °C)	
0.0	С	-1.0	0	30	G
1.0	M	-10	1	60	H
1.5	P	-100	2	120	J
2.2	R	-1000	3	250	K
3.3	S	-10000	4	500	Ĺ
4.7	Ŧ	+1	5	1000	M
7.5	U	+10	6	2500	N
		+100	7	1	
		+1000	8		
		+10000	9		

For Class IT Dielectrics

Low Temp. Symbol	High Temp. Symbol	Max. %△C Symbol	
-55°C X	+45°C 3	±1.0%	Α
-30°C Y	+65°C 4	±1.2%	В
+10°C Z	+85°C 5	±2.2%	C
	+105°C 6	±3.3%	D
	+125°C 7	±4.7%	E
	+150 °C 8	±7.5%	F
	+200 °C 9	±10.0%	P
		±15.0%	R
		±22.0%	S
		+22% /-33%	T
		+22% /-56%	U
		+22% /-82%	V

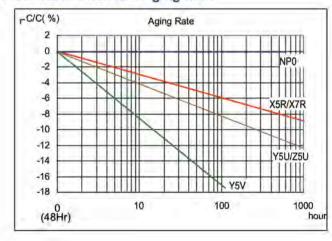
Ex.: C0G Negative U2J Negative 0±30ppm/ °C 750±120ppm/ °C

Ex.: X7R -55 ~ +125 °C ±15% Y5V -30 ~ +85 °C +22%/-82%

Operating Temperature Range

Class	EIA Symbol	Dielectric Code	Temperature Range(°C)	Capacitance Change	Reference Temperature
1	NP0	N	-55°C ~ +125 °C	0±30 ppm/°C	25°C
	SL	Ľ	-25°C ~ +85 °C	+350/-1000 ppm/°C	25°C
п	X7R	X	-55°C ~ +125°C	±15%	25°C
	X6S	S	-55°C ~ +105°C	±22%	25°C
	X7P	P	-55°C ~ +125°C	±10%	25°C
	X7T	T	-55°C ~ +125°C	+22% /-33%	25°C
	X7S	R	-55°C ~ +125°C	±22%	25°C
	X5R	В	-55°C ~ +85°C	±15%	25°C

♦ Dielectric Material – Aging Rate



Aging Rate

NP0: 0

X7R/X5R: 1 ~ 4 % / decade Y5U/Z5U: 4~6% / decade

Y5V: 6~10 % / decade

After performing De-Aging at 150±5 °C for 30 minutes and

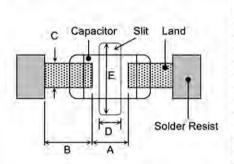
Placement at room temperature for 48 hours.



Construction of Board Pattern

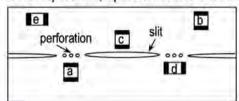
Improper circuit layout and pad/land size may cause poor solder joints between the component and the PC board. Insufficient solder may create a weak joint, and excessive solder may increase the potential for mechanical or thermal cracks in the ceramic capacitor. Therefore we recommend the solder pad/land size to be as shown in the following table:

1. Size and recommend land dimensions for reflow soldering



EIA Code	Chip (mm) La)	
EIA Code	1	W	Α	В	С	D	E
0201	0.60	0.30	0.2~0.3	0.2~0.4	0.2~0.4	-	
0402	1.00	0.50	0.3~0.5	0.3~0.5	0.4~0.6	-	-
0603	1.60	0.80	0.4~0.6	0.6~0.7	0.6~0.8		
0805	2.00	1.25	0.7~0.9	0.6~0.8	0.8~1.1		_
1206	3.20	1.60	2.2~2.4	0.8~0.9	1.0~1.4	1.0~2.0	3.2~3.7
1210	3.20	2.50	2.2~2.4	1.0~1.2	1.8~2.3	1.0~2.0	4.1~4.6
1808	4.60	2.00	2.8~3.4	1.8~2.0	1.5~1.8	1.0~2.8	3.6~4.1
1812	4.60	3.20	2.8~3.4	1.8~2.0	2.3~3.0	1.0~2.8	4.8~5.3
1825	4.60	6.35	2.8~3.4	1.8~2.0	5.1~5.8	1.0~4.0	7.1~8.3
2208	5.70	2.00	4.0~4.6	2.0~2.2	1.5~1.8	1.0~4.0	3.6~4.1
2211	5.70	2.80	4.0~4.6	2.0~2.2	2.0~2.6	1.0~4.0	4.4~4.9
2220	5.70	5.00	4.0~4.6	2.0~2.2	3.5~4.8	1.0~4.0	6.6~7.1
2225	5.70	6.35	4.0~4.6	2.0~2.2	5.1~5.8	1.0~4.0	7.1~8.3

 Mechanical strength varies according to location of chip capacitors on the P.C. board.
 Design the layout of components on the PC board in such a way to minimize the stress imposed on the components, upon flexure of the boards in depanelization or other processes.

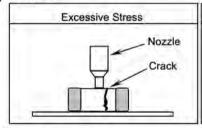


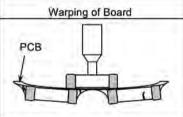
Component layout close to the edge of the board or the "depanelization line" is not recommended.

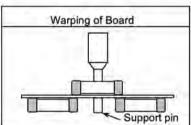
Susceptibility to stress is in the order of: a>b>c and d>e

Mounting

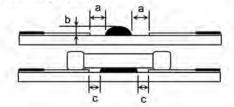
1. Sometimes cracking can be caused by the impact load of the pick and place nozzle. In the pick and place operation, if the low dead point is too low, excessive stress is applied to component. This may cause cracks in the ceramic capacitor, therefore it is required to move the low dead point of the nozzle to the higher level to minimize the board warpage and stress on the components. Nozzle pressure should be adjusted to 1N to 3N (static load) during the pick and place operation.







2. Amount of Adhesive



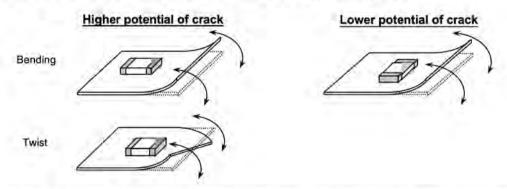
Example: 0805 & 1206

а	0.2mm min.	
b	70 ~ 100 μm	
C	Do not touch the solder land	



Handling after chip mounted

 Proper handling of the PCB is recommended since excessive bending and twisting of the PC board may induce mechanical stress and cause internal cracking of the capacitor.

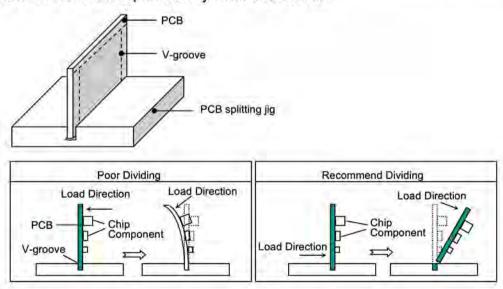


2. There is a potential of cracking if board is warped due to excessive load from the check pin.



3. Examples of PCB de-panelization jigs:

The outline of PCB breaking jig is shown below. It is recommended when dividing or breaking PCB that they are held near the jig where no bending will occur, this way there will be no compressive stress is applied to the capacitors on the PCB. Do not hold the PCB at a position which is far away from the jig, tensile stress to the capacitors may cause them to crack.

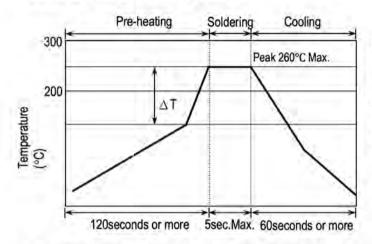




Soldering

1. Wave Soldering

Most of components are wave soldered with solder at Peak Temperature. Adequate care must be taken to prevent the potential of thermal cracks on the ceramic capacitors. Refer to the soldering methods below for optimum soldering benefits.



Soldering Method	Peak Temp.(°C) / Duration (sec)
1206/0805/0603	ΔT ≤ 100~150°C max.
Pb-Sn Solder	250°C(max.) / 3sec(max.)
Lead Free Solder	260°C(max.) / 5sec(max.)

Recommended solder compositions Sn-37Pb (Pb - Sn Solder) Sn-3.0Ag-0.5Cu (Lead Free Solder)

To optimize the result of soldering, proper preheating is essential:

- 1) Preheat temperature is too low
 - a. Flux flows to easily
 - b. Possibility of thermal cracks
- 2) Preheat temperature is too high
 - a. Flux deteriorates even when oxide film is removed
 - b. Causes warping of circuit board
 - c. Loss of reliability in chip and other components

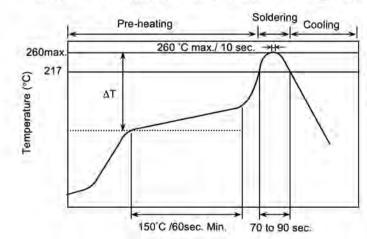
Cooling Condition:

Natural cooling in air is recommended. Forced cooling should be avoided, however if the chips are dipped into a solvent for cleaning, the temperature difference (ΔT) between the solvent and the chips must be less than 100°C.

2. Reflow Soldering

Preheat and gradual increase in temperature to the reflow temperature is recommended to decrease the potential of thermal crack on the components. The recommended heating rate depends on the size of component, however it should not exceed 3°C/Sec.

Recommend reflow profile for Lead-Free soldering temperature Profile (J-STD-020D)



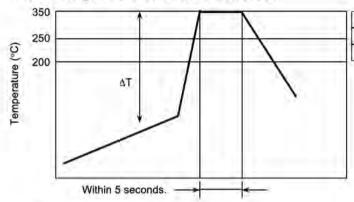
Soldering Method	Change in Temp.(°C)
1206 and Under	ΔT ≤190°C
1210 and Over	ΔT ≤130°C

* The cycles of soldering : Twice (Max.)



3. Hand Soldering

Sudden temperature changes in ceramic capacitors will result in a temperature gradient within the component and therefore may cause internal thermal cracking. In general a hand soldering method is not recommended unless proper preheating and handling practices have been taken. Care must also be taken not to touch the ceramic body of the capacitor with the tip of solder Iron. The soldering iron tip should always be placed on to the solder pad.



Soldering Method	Change in Temp.(°C	
1206 and Under	ΔT ≤150°C	
1210 and Over	ΔT ≤130°C	

How to Solder Repair by Solder Iron

- Selection of the soldering iron tip
 The required temperature of soldering iron for any type of repair depends on the type of the tip, the substrate material, and the solder land size.
- 2) recommended solder iron condition
 - a.) Preheating Condition: Board and components should be preheated sufficiently at 150°C or over, and soldering should be conducted with soldering iron as boards and components are maintained at sufficient temperatures
 - b.) Soldering iron power shall not exceed 30 W.
 - c.) Soldering iron tip diameter shall not exceed 3mm.
 - d.) Temperature of the iron tip shall not exceed 350°C, and the process should be finished within 5 seconds. (refer to MIL-STD-202G)
 - e.) Do not touch the ceramic body with the tip of solder iron. Direct contact of the soldering iron tip to ceramic body may cause thermal cracks.
 - f.) After soldering operation, let the products should be allowed to cool down naturally in air.

Storage

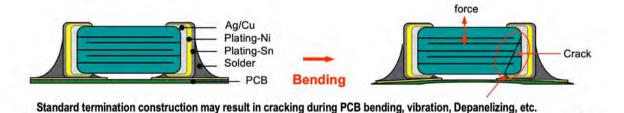
Store the capacitors where the temperature and relative humidity do not exceed 40°C and 70%RH. We recommend that the capacitors be used within 12 months from the date of manufacturing. Store the products in the original package and do not open the outer polyethylene bag until just before usage. If it is open, seal it as soon as possible or keep it in a desiccant with a desiccation agent.



Internal MLCC cracking can result in serious failure modes. If ceramic capacitors are subjected to severe mechanical stress, a bending crack may occur. This crack can run through two or more electrodes of opposing polarity and result in a short circuit. Typical bending cracks are shown below. In the worst-case short scenario. these circuits may lead to the MLCC overheating and catastrophic failure.

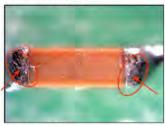


Typical Applications are power circuit input and output filtering, smoothing...

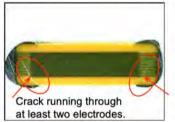


Actual Examples:

Failure Mode Type 1



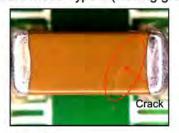
Surface View



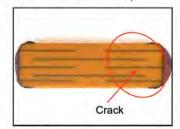
Crack

Cross Section View

Failure Mode Type 2 (wetting greater than 2/3 of thickness)



Top View



Cross Section View

The failure mode results from PCB bending forces. These cracks may not be visible on the MLCC surface. Cross sectional analysis is required to determine these internal cracks.

MLCC cracking frequently occurs during the circuit board depanelizing process. The root cause is knife (blade) vibration during the process.



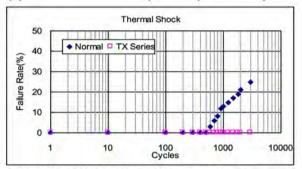
Holy Stone has developed the "Super Term" Series (TX suffix in the part number), which incorporates a "polymer layer" in the termination structure. This construction effectively absorbs external forces, reduces the incidence of cracking and improves overall product reliability. The Super Term product design is suitable for the applications including: high temperature automotive, power circuits and other critical end products with extreme processing conditions.

TX Product



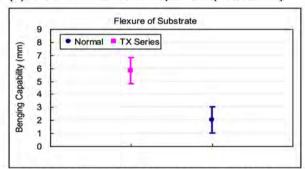
Reliability/Durability Comparison

(a) Thermal Shock Comparison (0805/X7R)

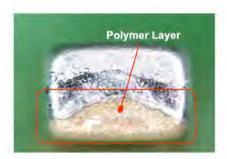


Thermal shock test on standard termination results in inception of failure at 500 cycles. Super Term TX Series reliability improves to over 3000 cycles.

(b) Substrate Flexure Comparison (0805/X7R)



Bending test on Super Term shows an improvement of about 5.0 mm bend vs. an average of about 2.0 mm. for standard termination.



During destructive bending test, the PCB is subjected to bending until capacitor failure. With Super Term there is no cracking damage in the ceramic. Super Term effectively prevents ceramic body cracking during extreme mechanical stress as simulated by this test.



Super Term failures resulting from destructive bending test tend to occur in the OPEN mode and not short circuit mode typical of standard termination failures. The Super Term (polymer layer) design is a polymer material and can be seen in the above photo.



MLCC Arc Prevention - for Hi-Pot Testing

Due to the open and porous nature of the surface of the X7R dielectric. moisture and/or dirt which will have a lower resistance than the dielectric grains, can become entrapped in the surface. Dirt can also include any flux residues as a result of the soldering process. This dirt/flux as well as becoming entrapped into the surface will, in itself, attract additional moisture onto the surface thus reducing the surface resistance and the voltage at which arcing occurs. Surface arcing or flashover at worst can cause equipment failure during isolation testing and, in addition, will leave a carbon track on the surface which can lead to eventual failure of the capacitor.



Typical Applications for telecommunication devices(IEEE802.3) in LAN interface, Ballast...

Holy Stone has developed an Arc Prevention coating process that coats the surface of the dielectric without encroaching onto the termination material. This coating makes the surface of the dielectric non porous and prevents moisture and dirt becoming trapped thus reducing the surface resistance and the arcing voltage of the capacitor. It has been shown that the arcing voltage can be increased by up to 65% on soldered parts by using the Holy Stone Arc Prevention Coating.



Typical surface arcing on X7R MLCC is from termination-to-termination (shown in polarized light)

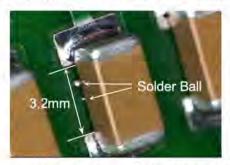
NP0 & X7R Material Characteristic Comparison

Item	NP0	X7R	
Dielectric Constant	30 ~ 100	2000 ~ 4000	
I. Resistance	>10 ¹³ Ω	>10 ¹¹ Ω	
B.D. Voltage	70~80 Vdc/um	40~50 Vdc/um	
Grain Size	< 500nm	900nm ~ 1500nm	
Grain Size (x8000)			
Porosity (x1000)			

- The different grain shape and size will lead to different grain density after sintering.
- The grain size of NP0 dielectric is smaller than that of X7R resulting a denser and less porous structure.
- Surface porosity will trap dirt, flux and moisture causing the surface resistance to dramatically decrease.
- Low surface resistance will cause the arcing voltage to reduce, possibly leading to failure of the capacitor or equipment during isolation testing.
- Using the Holy Stone Arc Prevention coating effectively makes the surface of X7R dielectric similar to that of NP0.

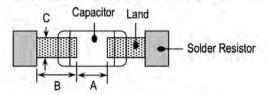


Creepage distance v.s. Arcing Voltage



Solder balls reduce the creepage distance between terminations and thus reduce the arcing voltage

Recommended Solder Pad Design



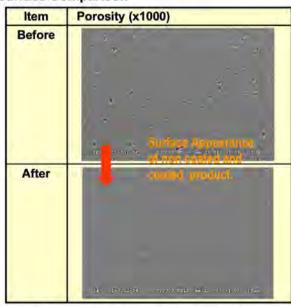
EIA Code	Chip (mm)		Land (mm)		
	L L	W	A	В	С
1808	4.6±0.3	2.0 ±0.2	3.2~3.6	1.2~2.4	1.5~1.8
1812	4.6±0.3	3.2 ±0.2	3.2~3.6	1.2~2.4	2.3~3.0
2208	5.7±0.4	2.0 ±0.2	4.0~4.6	1.2~2.4	1.5~1.8
2211	5.7±0.4	2.0 ±0.3	4.0~4.6	1.2~2.4	2.0~2.6
2220	5.7±0.4	5.0 ±0.4	4.0~4.6	1.2~2.4	3.5~4.8

The distance between terminations also has a direct effect on the arcing voltage. The greater the distance (chip size) the higher this voltage will be. Solder pad design will have a significant effect on the arcing of high voltage capacitors. Above is our recommended solder pad land design or each chip size.

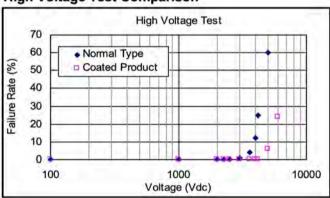
Arc Prevention Coating

The application of the Arc Prevention coating, which is a high insulation resistance material, coats the surface of the dielectric with a smooth and non porous layer that prevents moisture entering the surface pours and also aids cleaning post soldering. The following diagrams show the difference between coated and non coated X7R components.

Surface Comparison



High Voltage Test Comparison



- The coating reduces the porosity of the X7R surface and provides a smooth surface which help prevent surface arcing.
- The maximum Hi-pot test level will be increased by >1000Vdc after coating with both unsoldered and soldered components.

Using the Holy Stone Arc Prevention coating increases the surface arcing voltage of X7R capacitors to almost that of an equivalent NP0 part. However, the higher dielectric constant of X7R allows for higher capacitance values to be achieved in any given case size.

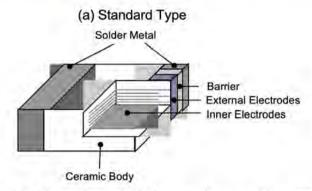
The Holy Stone Arc Prevention coating provides a total solution to the harmful effects of surface arcing.



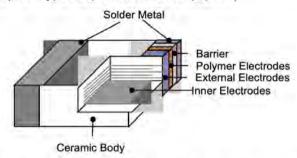
Description:

MLCC: NP0, X7R, X7S, X5R, X7P, X7T, X6S dielectrics.

Basic Construction/Homogenous Material:







- Multi-layer Ceramic Chip capacitors are Homogenous devices manufactured from materials that cannot be mechanically disjointed into different materials.
- Multi-layer Ceramic Chip products: Standard sizes are fully RoHS compliant.

RoHS Status	Lead-Free Status / MSL level
※ External plating : 100% Matte Sn as Standard	Pb-free Reflow & Wave Solder compliant, MSL=1 Reflow: 260°C max recommended Wave: 260°C max recommended Wave & Reflow profile refer to IHHEC recommended solder profile.

Part Number Designation: (Generally no change to P/N, but available as P/N prefix at customer request)

Product Marking:

(available at customer request, highlighted or marked on reel and container)

RoHS Compliant : No Change to P/N Pb-Free : No Change to P/N

Pb free: Pb free



refer to JEDEC&IPC Std.











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